GOA UNIVERSITY
Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - II EXAMINATION HELD IN MAY 2016

Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3201 P R No : 201507401 Sex : M Name : BHASALE VINAY BHANUDAS

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	BB	P	
	IA	2	AB	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	BB	P	
	IA	2	AB	P	
Design for Testability & E-Waste Management					
	Theory	4	BC	P	
	IA	2	AB	P	
Processor Architecture & Parallel Processing					
	Theory	4	CC	P	
	IA	2	BB	P	
Memory Design					
	Theory	4	BB	P	
	IA	2	AA	P	
Parallel Processing Lab					
	IA	2	AB	P	
	Practical	2	BC	P	
FPGA & Embedded Systems Lab					
	IA	2	AB	P	
	Practical	2	AA	P	
	Total :	38			7.11 P PASSES

Seat No : 3202 P R No : 201107681 Sex : M Name : BORKAR NEEL RAMAKANT

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	AA	P	
	IA	2	AA	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	BB	P	
	IA	2	AB	P	
Design for Testability & E-Waste Management					
	Theory	4	AB	P	
	IA	2	AO	P	
Processor Architecture & Parallel Processing					
	Theory	4	BC	P	
	IA	2	BB	P	
Memory Design					
	Theory	4	AB	P	
	IA	2	AA	P	
Parallel Processing Lab					
	IA	2	BB	P	
	Practical	2	BC	P	
FPGA & Embedded Systems Lab					
	IA	2	AB	P	
	Practical	2	AA	P	
	Total :	38			7.84 P PASSES

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

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Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3203 P R No : 201108381 Sex : F Name : GOVEKAR DIVYA DIGAMBAR
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA				
	Theory	4	BC P	
	IA	2	BC P	
Digital Signal Processors & Embedded Systems				
	Theory	4	CC P	
	IA	2	CC P	
Design for Testability & E-Waste Management				
	Theory	4	CC P	
	IA	2	BC P	
Processor Architecture & Parallel Processing				
	Theory	4	FF F	
	IA	2	CC P	
Memory Design				
	Theory	4	CC P	
	IA	2	BC P	
Parallel Processing Lab				
	IA	2	BC P	
	Practical	2	CC P	
FPGA & Embedded Systems Lab				
	IA	2	AB P	
	Practical	2	AB P	
	Total :	38		5.11 F FAILS

Seat No : 3204 P R No : 201006305 Sex : F Name : KALSHAONKAR REEMA DATTARAM
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA				
	Theory	4	AA P	
	IA	2	AA P	
Digital Signal Processors & Embedded Systems				
	Theory	4	BB P	
	IA	2	AA P	
Design for Testability & E-Waste Management				
	Theory	4	AA P	
	IA	2	AA P	
Processor Architecture & Parallel Processing				
	Theory	4	AB P	
	IA	2	AB P	
Memory Design				
	Theory	4	AA P	
	IA	2	AO P	
Parallel Processing Lab				
	IA	2	AA P	
	Practical	2	AB P	
FPGA & Embedded Systems Lab				
	IA	2	AB P	
	Practical	2	AO P	
	Total :	38		8.63 P PASSES

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Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3205 P R No : 200907827 Sex : F Name : KHANDEPARKAR SHWETA MAHENDRA
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	AA	P	
	IA	2	AA	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	AB	P	
	IA	2	AA	P	
Design for Testability & E-Waste Management					
	Theory	4	BB	P	
	IA	2	AA	P	
Processor Architecture & Parallel Processing					
	Theory	4	AA	P	
	IA	2	AB	P	
Memory Design					
	Theory	4	AB	P	
	IA	2	AO	P	
Parallel Processing Lab					
	IA	2	AA	P	
	Practical	2	BB	P	
FPGA & Embedded Systems Lab					
	IA	2	AA	P	
	Practical	2	AA	P	
	Total :	38			8.47 P PASSES

Seat No : 3206 P R No : 201008499 Sex : F Name : KHARANGATE LAXIMI SHASHANK
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	AA	P	
	IA	2	AA	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	AB	P	
	IA	2	AA	P	
Design for Testability & E-Waste Management					
	Theory	4	AB	P	
	IA	2	AB	P	
Processor Architecture & Parallel Processing					
	Theory	4	BB	P	
	IA	2	AB	P	
Memory Design					
	Theory	4	AA	P	
	IA	2	AA	P	
Parallel Processing Lab					
	IA	2	AA	P	
	Practical	2	BB	P	
FPGA & Embedded Systems Lab					
	IA	2	AA	P	
	Practical	2	AA	P	
	Total :	38			8.37 P PASSES

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Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3207 P R No : 201105539 Sex : M Name : KORDE CHARUDATTA GURUDAS

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	AB	P	
	IA	2	AA	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	BB	P	
	IA	2	AB	P	
Design for Testability & E-Waste Management					
	Theory	4	BC	P	
	IA	2	AB	P	
Processor Architecture & Parallel Processing					
	Theory	4	CC	P	
	IA	2	AA	P	
Memory Design					
	Theory	4	AB	P	
	IA	2	AA	P	
Parallel Processing Lab					
	IA	2	AA	P	
	Practical	2	AB	P	
FPGA & Embedded Systems Lab					
	IA	2	AA	P	
	Practical	2	AB	P	
	Total :	38			7.63 P PASSES

Seat No : 3208 P R No : 201107707 Sex : M Name : MAHATME MOHIT BHAIRAV

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	BB	P	
	IA	2	BB	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	BC	P	
	IA	2	CC	P	
Design for Testability & E-Waste Management					
	Theory	4	BC	P	
	IA	2	AB	P	
Processor Architecture & Parallel Processing					
	Theory	4	CC	P	
	IA	2	BB	P	
Memory Design					
	Theory	4	BC	P	
	IA	2	AA	P	
Parallel Processing Lab					
	IA	2	AB	P	
	Practical	2	AB	P	
FPGA & Embedded Systems Lab					
	IA	2	AB	P	
	Practical	2	AB	P	
	Total :	38			6.74 P PASSES

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

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RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - II EXAMINATION HELD IN MAY 2016

Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3209 P R No : 201007353 Sex : F Name : MAYEKAR MITHILI NARAYAN

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA	
ASIC Design & FPGA						
	Theory	4	BB	P		
	IA	2	AB	P		
Digital Signal Processors & Embedded Systems						
	Theory	4	BB	P		
	IA	2	CC	P		
Design for Testability & E-Waste Management						
	Theory	4	BB	P		
	IA	2	CC	P		
Processor Architecture & Parallel Processing						
	Theory	4	CC	P		
	IA	2	CC	P		
Memory Design						
	Theory	4	CC	P		
	IA	2	AB	P		
Parallel Processing Lab						
	IA	2	BB	P		
	Practical	2	CC	P		
FPGA & Embedded Systems Lab						
	IA	2	BB	P		
	Practical	2	BB	P		
	Total :	38			6.26 P	Result Reserve Sem I Not Passed

Seat No : 3210 P R No : 201006308 Sex : F Name : NAIK STHRIGDHARA NAVANATH

No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA	
ASIC Design & FPGA						
	Theory	4	BB	P		
	IA	2	AA	P		
Digital Signal Processors & Embedded Systems						
	Theory	4	BC	P		
	IA	2	BB	P		
Design for Testability & E-Waste Management						
	Theory	4	AB	P		
	IA	2	BC	P		
Processor Architecture & Parallel Processing						
	Theory	4	FF	F		
	IA	2	BB	P		
Memory Design						
	Theory	4	AA	P		
	IA	2	AB	P		
Parallel Processing Lab						
	IA	2	AB	P		
	Practical	2	CC	P		
FPGA & Embedded Systems Lab						
	IA	2	BB	P		
	Practical	2	BB	P		
	Total :	38			6.53 F FAILS	

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

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RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - II EXAMINATION
HELD IN MAY 2016 Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3211 P R No : 201008518 Sex : F Name : NAIK TEJA UDAY
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA	
ASIC Design & FPGA						
	Theory	4	AB	P		
	IA	2	AA	P		
Digital Signal Processors & Embedded Systems						
	Theory	4	BC	P		
	IA	2	BC	P		
Design for Testability & E-Waste Management						
	Theory	4	BB	P		
	IA	2	BC	P		
Processor Architecture & Parallel Processing						
	Theory	4	CC	P		
	IA	2	BB	P		
Memory Design						
	Theory	4	AB	P		
	IA	2	AB	P		
Parallel Processing Lab						
	IA	2	BB	P		
	Practical	2	CC	P		
FPGA & Embedded Systems Lab						
	IA	2	BB	P		
	Practical	2	BB	P		
	Total :	38			6.84 P PASSES	

Seat No : 3212 P R No : 201107719 Sex : M Name : PATIL ABHIJEET VILAS
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA	
ASIC Design & FPGA						
	Theory	4	BB	P		
	IA	2	BB	P		
Digital Signal Processors & Embedded Systems						
	Theory	4	BC	P		
	IA	2	CC	P		
Design for Testability & E-Waste Management						
	Theory	4	BC	P		
	IA	2	BC	P		
Processor Architecture & Parallel Processing						
	Theory	4	FF	F		
	IA	2	BB	P		
Memory Design						
	Theory	4	BC	P		
	IA	2	BB	P		
Parallel Processing Lab						
	IA	2	BB	P		
	Practical	2	CC	P		
FPGA & Embedded Systems Lab						
	IA	2	AB	P		
	Practical	2	AB	P		
	Total :	38			5.79 F	Result Reserve Sem I Not Passed

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

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Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3213 P R No : 201105565 Sex : M Name : RANE KARAN PARSHURAM
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA				
	Theory	4	AB P	
	IA	2	AA P	
Digital Signal Processors & Embedded Systems				
	Theory	4	BB P	
	IA	2	AA P	
Design for Testability & E-Waste Management				
	Theory	4	AB P	
	IA	2	AO P	
Processor Architecture & Parallel Processing				
	Theory	4	BC P	
	IA	2	AB P	
Memory Design				
	Theory	4	BB P	
	IA	2	AA P	
Parallel Processing Lab				
	IA	2	AA P	
	Practical	2	AA P	
FPGA & Embedded Systems Lab				
	IA	2	AA P	
	Practical	2	AA P	
	Total :	38		8.05 P PASSES

Seat No : 3214 P R No : 201106312 Sex : M Name : SAWAL SAGAR NAMDEV
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA				
	Theory	4	AB P	
	IA	2	BB P	
Digital Signal Processors & Embedded Systems				
	Theory	4	BB P	
	IA	2	CC P	
Design for Testability & E-Waste Management				
	Theory	4	BB P	
	IA	2	BB P	
Processor Architecture & Parallel Processing				
	Theory	4	AB P	
	IA	2	AB P	
Memory Design				
	Theory	4	BB P	
	IA	2	AB P	
Parallel Processing Lab				
	IA	2	AA P	
	Practical	2	BB P	
FPGA & Embedded Systems Lab				
	IA	2	AB P	
	Practical	2	AB P	
	Total :	38		7.42 P PASSES

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

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Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3215 P R No : 201007394 Sex : M Name : SHETGAONKAR ANAY ANIL

No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA	
ASIC Design & FPGA					
	Theory	4	BC P		
	IA	2	BB P		
Digital Signal Processors & Embedded Systems					
	Theory	4	CC P		
	IA	2	CC P		
Design for Testability & E-Waste Management					
	Theory	4	FF F		
	IA	2	CC P		
Processor Architecture & Parallel Processing					
	Theory	4	FF F		
	IA	2	BC P		
Memory Design					
	Theory	4	FF F		
	IA	2	CC P		
Parallel Processing Lab					
	IA	2	BB P		
	Practical	2	BC P		
FPGA & Embedded Systems Lab					
	IA	2	CC P		
	Practical	2	BC P		
	Total :	38		3.89 F	Result Reserve Sem I Not Passed

Seat No : 3216 P R No : 200801358 Sex : F Name : PARKER NEHA PRASAD

No Of Attempts : 2

		No Of Credits	Grade Obtained	SGPA	
ASIC Design & FPGA					
	Theory	4	AA P		
	IA	2	AA +		
Digital Signal Processors & Embedded Systems					
	Theory	4	BC P		
	IA	2	BB +		
Design for Testability & E-Waste Management					
	Theory	4	AB P		
	IA	2	AA +		
Processor Architecture & Parallel Processing					
	Theory	4	FF F		
	IA	2	BB +		
Memory Design					
	Theory	4	AB P		
	IA	2	AA +		
Parallel Processing Lab					
	IA	2	BC +		
	Practical	2	BC +		
FPGA & Embedded Systems Lab					
	IA	2	AB +		
	Practical	2	AB +		
	Total :	38		6.89 F FAILS	

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By : [signature]

Checked By : [signature]

Date : 10/8/16

10/8/2016
S.S.J. Figueiredo
Assistant Registrar-E(Proff.)

10/8/2016
for Controller Of Examinations

Prof. Y. V. Reddy
Registrar

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