

F-04 ELECTRICAL & ELECTRONIC
COLLEGE : GDA COLLEGE OF ENGINEERING

** RC0007-08
EXAM YEAR : DEC-2012

SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD	VI				MAX. 100	25	125	50 OR 25	50 OR 25	
PAPER DESCRIPTION						MIN. 040		50	20 OR 10	20 OR 10	MAX. 850 MIN. 340

0257 200907847 (A) N F DIAS MARIA FLEUR DOMINICA

VLSI CIRCUIT DESIGN	067	020	087P	041P
ELECTRICAL POWER SYSTEM II	068	021	089P	
ADVANCED CONTROLLED DRIVES	061	025	086P	
NEURAL NETWORKS & FUZZY LOGIC	086	025	111P	044P
FLEXIBLE AC TRANSMISSION SYSTEM	071	020	091P	039P
PROJECT		024P		048P

** REVALUATION CASE **

660 P

0258 200907829 (A) N F FERNANDES ALLISTON SANTOS

VLSI CIRCUIT DESIGN	064	015	079P	038P
ELECTRICAL POWER SYSTEM II	065	020	085P	
ADVANCED CONTROLLED DRIVES	068	021	089P	
NEURAL NETWORKS & FUZZY LOGIC	071	016	087P	042P
FLEXIBLE AC TRANSMISSION SYSTEM	059	015	074P	036P
PROJECT		020P		041P

** REVALUATION CASE **

591 P

0260 200908070 (A) N M GANGMEI GAISHINTHUI

VLSI CIRCUIT DESIGN	063	013	076P	038P
ELECTRICAL POWER SYSTEM II	063	014	077P	
ADVANCED CONTROLLED DRIVES	059	014	073P	
NEURAL NETWORKS & FUZZY LOGIC	059	013	072P	039P
FLEXIBLE AC TRANSMISSION SYSTEM	051	018	069P	038P
PROJECT		024P		048P

** REVALUATION CASE **

554 #10 P

0261 200908005 (A) N F GAUNS DESAI ASHWINI ABHAY

VLSI CIRCUIT DESIGN	055	017	072P	038P
ELECTRICAL POWER SYSTEM II	063	019	082P	
ADVANCED CONTROLLED DRIVES	050	015	065P	
NEURAL NETWORKS & FUZZY LOGIC	050	015	066P	039P
FLEXIBLE AC TRANSMISSION SYSTEM	062	014	076P	035P
PROJECT		022P		044P

** REVALUATION CASE **

640 P

0262 200907885 (A) N M GAWAB VIJESH GANESH

VLSI CIRCUIT DESIGN	050	010	060P	033P
ELECTRICAL POWER SYSTEM II	063	010	081P	
ADVANCED CONTROLLED DRIVES	042	010	052P	
NEURAL NETWORKS & FUZZY LOGIC	056	010	076P	038P
FLEXIBLE AC TRANSMISSION SYSTEM	061	011	072P	033P
PROJECT		022P		044P

** REVALUATION CASE **

511 P

I-06 ELECTRICAL & ELECTRONIC
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC2007-08 **
EXAM YEAR : DEC-2012

SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
						MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 850
						MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0243 200800872 (A) N M ALWIN NORDNHA
 VLBI CIRCUIT DESIGN 035 #05 013 048P#05 038P
 ELECTRICAL POWER SYSTEM II 046 011 057P
 ADVANCED CONTROLLED DRIVES 035 #05 015 050P#05
 NEURAL NETWORKS & FUZZY LOGIC 044 013 057P 038P
 FLEXIBLE AC TRANSMISSION SYSTEM 040 014 054P 038P
 PROJECT 024P 048P
 ** REVALUATION CASE **

0244 200907838 (A) N F ANVEKAR ANITA ANIL
 VLBI CIRCUIT DESIGN 060 021 089P 041P
 ELECTRICAL POWER SYSTEM II 067 020 087P
 ADVANCED CONTROLLED DRIVES 069 022 091P
 NEURAL NETWORKS & FUZZY LOGIC 066 018 084P 043P
 FLEXIBLE AC TRANSMISSION SYSTEM 063 021 084P 037P
 PROJECT 020P 040P
 ** REVALUATION CASE **

0245 200804571 (A) N F ARATI GUPTA
 VLBI CIRCUIT DESIGN 049 014 063P 037P
 ELECTRICAL POWER SYSTEM II 060 017 077P
 ADVANCED CONTROLLED DRIVES 059 010 069P
 NEURAL NETWORKS & FUZZY LOGIC 080 017 097P 038P
 FLEXIBLE AC TRANSMISSION SYSTEM 070 013 083P 032P
 PROJECT 020P 041P
 ** REVALUATION CASE **

0246 200907846 (A) N M BHANDANKAR VISHNU VISHWANATH
 VLBI CIRCUIT DESIGN 055 012 067P 035P
 ELECTRICAL POWER SYSTEM II 062 016 078P
 ADVANCED CONTROLLED DRIVES 068 015 083P
 NEURAL NETWORKS & FUZZY LOGIC 055 012 067P 043P
 FLEXIBLE AC TRANSMISSION SYSTEM 056 013 069P 033P
 PROJECT 023P 046P
 ** REVALUATION CASE **

0247 200607094 (A) N M BHAT PRADOSH MOHAN
 VLBI CIRCUIT DESIGN 023 012 035P 038E
 ELECTRICAL POWER SYSTEM II 042 007 049E #1
 ADVANCED CONTROLLED DRIVES 008 019 027P
 NEURAL NETWORKS & FUZZY LOGIC 027 010 037P 038E
 FLEXIBLE AC TRANSMISSION SYSTEM 037 #3 012 049E #3 031E
 PROJECT 022E 044E
 ** REVALUATION CASE **

06 ELECTRICAL & ELECTRONIC
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC2007-08 **

EXAM YEAR : DEC-2012

SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD.	VI.				MAX. 100	25	125	50 OR 25	50 OR 25	
PAPER DESCRIPTION						MIN. 040		50	20 OR 10	20 OR 10	MAX. 050 MIN. 340

0248	200907031	(A)	N	M	BHUKTI GAURAV DILIP						
					VLBI CIRCUIT DESIGN	043	011	054P		035P	
					ELECTRICAL POWER SYSTEM II	064	018	082P			
					ADVANCED CONTROLLED DRIVES	054	010	064P			
					NEURAL NETWORKS & FUZZY LOGIC	050	008	058P		035P	
					FLEXIBLE AC TRANSMISSION SYSTEM	047	012	059P		033P	
					PROJECT		022P			045P	

** REVALUATION CASE **

487

P

0249	200703655	(A)	N	M	CHAGAS PEREIRA JASON MARK STEFAN						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
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					VLBI CIRCUIT DESIGN	033	010	043P		030E	
					ELECTRICAL POWER SYSTEM II	038	#2	049E	#2		
					ADVANCED CONTROLLED DRIVES	036	#4	046E	#4		
					NEURAL NETWORKS & FUZZY LOGIC	028	012	040P		030E	
					FLEXIBLE AC TRANSMISSION SYSTEM	035		042P		033E	
					PROJECT		020E			038E	

** REVALUATION CASE **

0250	200907812	(A)	N	M	CHARI DEVENDRA DEVANAND						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
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					VLBI CIRCUIT DESIGN	040	013	053P		030P	
					ELECTRICAL POWER SYSTEM II	046	011	057P			
					ADVANCED CONTROLLED DRIVES	042	010	052P			
					NEURAL NETWORKS & FUZZY LOGIC	044	013	057P		038P	
					FLEXIBLE AC TRANSMISSION SYSTEM	047	012	059P		032P	
					PROJECT		021P			042P	

** REVALUATION CASE **

0255	200801181	(A)	N	M	DA COSTA EMANUEL ANTONIO SIMAO						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
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					VLBI CIRCUIT DESIGN	053	017	070P		031P	
					ELECTRICAL POWER SYSTEM II	061	020	081P			
					ADVANCED CONTROLLED DRIVES	062	025	087P			
					NEURAL NETWORKS & FUZZY LOGIC	065	017	082P		043P	
					FLEXIBLE AC TRANSMISSION SYSTEM	054	019	073P		036P	
					PROJECT		024P			048P	

** REVALUATION CASE **

577

P

0256	200807005	(A)	N	M	DHARGALKAR SUDHIR GOVIND						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
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					VLBI CIRCUIT DESIGN	026	012	038P		034E	
					ELECTRICAL POWER SYSTEM II	054	015	069E			
					ADVANCED CONTROLLED DRIVES	040	010	050E			
					NEURAL NETWORKS & FUZZY LOGIC	040	013	053E		026E	
					FLEXIBLE AC TRANSMISSION SYSTEM	046	015	061E		033E	
					PROJECT		021E			042E	

** REVALUATION CASE **

C-04 ELECTRICAL & ELECTRONIC
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC2007-08
EXAM YEAR : DEC-2012

SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
						MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 250
						MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0263 200703678 (A) N M GHADI AMIT TANAPPA ** RESULT WITHHELD (SEM. 5 NOT PASSED) **

VLSI CIRCUIT DESIGN	040	005	045P	#5	030P
ELECTRICAL POWER SYSTEM I	062	019	081P		
ADVANCED CONTROLLED DRIVES	047	010	057P		
NEURAL NETWORKS & FUZZY LOGIC	053	012	065P		040P
FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	037	#3	011	048P #3	030P
			022P		042P

** REVALUATION CASE **

0264 200907825 (A) N M GHANCHI SURESHKUMAR LAKMARAM

VLSI CIRCUIT DESIGN	050	015	065P		038P
ELECTRICAL POWER SYSTEM II	063	017	080P		
ADVANCED CONTROLLED DRIVES	046	022	068P		
NEURAL NETWORKS & FUZZY LOGIC	047	016	063P		040P
FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	043	014	057P		042P
			024P		048P

** REVALUATION CASE **

525 P

0265 200907842 (A) N F GODINHO AELREA BENDOVEVA

VLSI CIRCUIT DESIGN	063	016	079P		038P
ELECTRICAL POWER SYSTEM II	071	018	089P		
ADVANCED CONTROLLED DRIVES	081	021	102P		
NEURAL NETWORKS & FUZZY LOGIC	070	019	089P		043P
FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	066	014	080P		041P
			024P		048P

** REVALUATION CASE **

633 P

0266 200907832 (A) N M GOVENKAR SWETAN SURYAKANT

VLSI CIRCUIT DESIGN	063	021	084P		038P
ELECTRICAL POWER SYSTEM II	068	020	088P		
ADVANCED CONTROLLED DRIVES	069	025	094P		
NEURAL NETWORKS & FUZZY LOGIC	070	022	092P		044P
FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	066	020	086P		040P
			022P		043P

** REVALUATION CASE **

631 P

0267 200907840 (A) N M JADHAV VARUN PRATAPBINHA

VLSI CIRCUIT DESIGN	052	012	064P		035P
ELECTRICAL POWER SYSTEM II	052	017	069P		
ADVANCED CONTROLLED DRIVES	055	010	065P		
NEURAL NETWORKS & FUZZY LOGIC	055	008	063P		038P
FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	057	015	072P		036P
			019P		039P

** REVALUATION CASE **

500 P

66 ELECTRICAL & ELECTRONIC
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC2007-08 **
EXAM YEAR : DEC-2012

SEAT P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD.	VT.			MAX. 100	25	125	50 OR 25	50 OR 25	
	PAPER DESCRIPTION				MIN. 040		50	20 OR 10	20 OR 10	MAX. 250 MIN. 70

0268 200907804 (A) N M JALMI BIDDHANT KASHINATH

VLSI CIRCUIT DESIGN	060	017	077P	035P
ELECTRICAL POWER SYSTEM II	063	016	079P	
ADVANCED CONTROLLED DRIVES	050	021	071P	
NEURAL NETWORKS & FUZZY LOGIC	051	017	068P	041P
FLEXIBLE AC TRANSMISSION SYSTEM	070	020	090P	039P
PROJECT		024P		048P

** REVALUATION CASE **

572 #12 P

0269 200907837 (A) N M KADNEKAR VINAYAK ALIAS BIDDHANT PANDURANG

** RESULT WITHHELD - (SEM. & NOT PASSED) **

VLSI CIRCUIT DESIGN	042	005	047E	030E
ELECTRICAL POWER SYSTEM II	053	013	066E	
ADVANCED CONTROLLED DRIVES	044	010	054E	
NEURAL NETWORKS & FUZZY LOGIC	040	004	044E	028E
FLEXIBLE AC TRANSMISSION SYSTEM	035	004	039E	030E
PROJECT		018E		036E

** REVALUATION CASE **

0270 200907826 (A) N F KALANGUTKAR MEGHNA VIJAY

VLSI CIRCUIT DESIGN	063	020	083P	045P
ELECTRICAL POWER SYSTEM II	068	020	088P	
ADVANCED CONTROLLED DRIVES	073	020	093P	
NEURAL NETWORKS & FUZZY LOGIC	071	017	088P	042P
FLEXIBLE AC TRANSMISSION SYSTEM	061	018	079P	039P
PROJECT		020P		041P

** REVALUATION CASE **

618 P

0271 200907805 (A) N F KAMAT PRAGATI PRABHAKAR

VLSI CIRCUIT DESIGN	059	010	077P	032P
ELECTRICAL POWER SYSTEM II	072	012	084P	
ADVANCED CONTROLLED DRIVES	043	016	059P	
NEURAL NETWORKS & FUZZY LOGIC	052	016	068P	040P
FLEXIBLE AC TRANSMISSION SYSTEM	054	016	070P	036P
PROJECT		022P		044P

** REVALUATION CASE **

532 P

0274 200908068 (A) N M M PRAKASH REDDY

VLSI CIRCUIT DESIGN	068	020	088P	045P
ELECTRICAL POWER SYSTEM II	069	019	088P	
ADVANCED CONTROLLED DRIVES	063	025	088P	
NEURAL NETWORKS & FUZZY LOGIC	087	021	110P	043P
FLEXIBLE AC TRANSMISSION SYSTEM	091	022	113P	045P
PROJECT		020P		041P

** REVALUATION CASE **

681 P

06 ELECTRICAL & ELECTRONIC
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC2007-08 **
EXAM YEAR : DEC-2012

SEAT P.R. NO.	ARE PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD.	VI.		MAX. 100	25	125	50 OR 25	50 OR 25	
			PAPER DESCRIPTION	MIN. 040		50	20 OR 10	20 OR 10	MAX. 850 MIN. 340

0276 200907854 (A) N F MANATHI SWETA VIJAY

VLSI CIRCUIT DESIGN	048	021	069P	045P
ELECTRICAL POWER SYSTEM II	063	022	085P	
ADVANCED CONTROLLED DRIVES	047	018	065P	
NEURAL NETWORKS & FUZZY LOGIC	068	021	089P	044P
FLEXIBLE AC TRANSMISSION SYSTEM	048	017	065P	040P
PROJECT		024P		048P

** REVALUATION CASE **

574 #12 P

0278 200801055 (A) N F MHAMAL MEGHA MOHAN

VLSI CIRCUIT DESIGN	053	014	067P	030P
ELECTRICAL POWER SYSTEM II	064	021	085P	
ADVANCED CONTROLLED DRIVES	062	021	083P	
NEURAL NETWORKS & FUZZY LOGIC	060	015	075P	035P
FLEXIBLE AC TRANSMISSION SYSTEM	067	021	088P	038P
PROJECT		022P		043P

** REVALUATION CASE **

566 P

0279 201004506 (A) N M MOHAMMED AZAL ABULKASIM MULLA

VLSI CIRCUIT DESIGN	039	#1	006	045P	#5	030P
ELECTRICAL POWER SYSTEM II	050		013	063P		
ADVANCED CONTROLLED DRIVES	046		010	056P		
NEURAL NETWORKS & FUZZY LOGIC	042		005	047P	#3	030P
FLEXIBLE AC TRANSMISSION SYSTEM	041		005	046P	#4	033P
PROJECT			020P			038P

** RESULT WITHHELD - (SEM. 5 NOT PASSED) **

** REVALUATION CASE **

0280 200907841 (A) N M NADKARNI KALPEET PRASAD

VLSI CIRCUIT DESIGN	046	017	063P	042P
ELECTRICAL POWER SYSTEM II	067	019	086P	
ADVANCED CONTROLLED DRIVES	050	021	071P	
NEURAL NETWORKS & FUZZY LOGIC	051	014	065P	040P
FLEXIBLE AC TRANSMISSION SYSTEM	059	014	073P	039P
PROJECT		023P		045P

** REVALUATION CASE **

547 P

0281 201004698 (A) N M NAIK CHETAN TULSIDAS

VLSI CIRCUIT DESIGN	058	014	072P	038P
ELECTRICAL POWER SYSTEM II	062	017	079P	
ADVANCED CONTROLLED DRIVES	048	010	058P	
NEURAL NETWORKS & FUZZY LOGIC	063	013	076P	040P
FLEXIBLE AC TRANSMISSION SYSTEM	050	017	067P	034P
PROJECT		020P		030P

** REVALUATION CASE **

524 P

06 ELECTRICAL & ELECTRONIC
COLLEGE : GOA COLLEGE OF ENGINEERING

** RCR007-08 **
EXAM YEAR : DEC-2012

SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY MAX. 100 MIN. 040	SESSIONAL 05	TOTAL 105 50	PRACTICAL 50 OR 25 20 OR 10	ORAL 50 OR 25 20 OR 10	REMARKS
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0282 200907821 (A) N F NAIK DIBHA DILEEP

VLSI CIRCUIT DESIGN	042	018	060P	039P
ELECTRICAL POWER SYSTEM II	062	023	085P	
ADVANCED CONTROLLED DRIVES	067	015	082P	
NEURAL NETWORKS & FUZZY LOGIC	068	015	083P	040P
FLEXIBLE AC TRANSMISSION SYSTEM	061	018	079P	034P
PROJECT		022P		044P

** REVALUATION CASE **

568 P

0283 200807636 (A) N M NAIK GAURESH NARAHARI

** RESULT WITHHELD - (SEM. 5 NOT PASSED) **

VLSI CIRCUIT DESIGN	040	015	055P	037P
ELECTRICAL POWER SYSTEM II	067	019	086P	
ADVANCED CONTROLLED DRIVES	052	020	072P	
NEURAL NETWORKS & FUZZY LOGIC	040	013	053P	038P
FLEXIBLE AC TRANSMISSION SYSTEM	053	016	069P	032P
PROJECT		022P		042P

** REVALUATION CASE **

0284 200907844 (A) N F NAIK NAVTI SHRADHA VISHNU

VLSI CIRCUIT DESIGN	061	022	083P	045P
ELECTRICAL POWER SYSTEM II	067	022	089P	
ADVANCED CONTROLLED DRIVES	060	021	081P	
NEURAL NETWORKS & FUZZY LOGIC	078	023	101P	044P
FLEXIBLE AC TRANSMISSION SYSTEM	067	020	087P	039P
PROJECT		023P		046P

** REVALUATION CASE **

638 P

0285 200703731 (A) N M NAIK RAJDEEP ROHIDAS

VLSI CIRCUIT DESIGN	045	013	058P	035P
ELECTRICAL POWER SYSTEM II	070	014	084P	
ADVANCED CONTROLLED DRIVES	048	014	062P	
NEURAL NETWORKS & FUZZY LOGIC	042	013	055P	038P
FLEXIBLE AC TRANSMISSION SYSTEM	044	016	060P	034P
PROJECT		022P		041P

** REVALUATION CASE **

489 P

0286 200907809 (A) N F NAIK SANJIVANI BHARAT

VLSI CIRCUIT DESIGN	069	025	094P	047P
ELECTRICAL POWER SYSTEM II	073	022	095P	
ADVANCED CONTROLLED DRIVES	082	025	107P	
NEURAL NETWORKS & FUZZY LOGIC	087	025	112P	047P
FLEXIBLE AC TRANSMISSION SYSTEM	069	022	091P	044P
PROJECT		023P		047P

** REVALUATION CASE **

707 P

06 ELECTRICAL & ELECTRONIC
COLLEGE : GOA COLLEGE OF ENGINEERING

RC2007-0A
EXAM YEAR : DEC-2012

BEAT P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD	VI			MAX. 100	25	125	50 OR 25	50 OR 25	
PAPER DESCRIPTION					MIN. 040		50	20 OR 10	20 OR 10	MAX. 850 MIN. 340

0287 200907810 (A) N N NAIK SARANG SHAMBHU

VLSI CIRCUIT DESIGN	047	012	059P	041P
ELECTRICAL POWER SYSTEM II	064	017	081P	
ADVANCED CONTROLLED DRIVES	061	014	075P	
NEURAL NETWORKS & FUZZY LOGIC	063	014	077P	040P
FLEXIBLE AC TRANSMISSION SYSTEM	051	018	069P	037P
PROJECT		023P		045P

** REVALUATION CASE **

547 P

0288 200907811 (A) N F NAIK BIDDHI DNYANESHWAR

VLSI CIRCUIT DESIGN	067	024	091P	045P
ELECTRICAL POWER SYSTEM II	066	025	091P	
ADVANCED CONTROLLED DRIVES	064	020	084P	
NEURAL NETWORKS & FUZZY LOGIC	071	022	093P	044P
FLEXIBLE AC TRANSMISSION SYSTEM	074	022	096P	041P
PROJECT		023P		046P

** REVALUATION CASE **

654 P

0289 200907815 (A) N F NAIK SONIA AJIT

VLSI CIRCUIT DESIGN	053	012	065P	038P
ELECTRICAL POWER SYSTEM II	065	013	078P	
ADVANCED CONTROLLED DRIVES	043	016	059P	
NEURAL NETWORKS & FUZZY LOGIC	066	017	083P	044P
FLEXIBLE AC TRANSMISSION SYSTEM	043	015	058P	034P
PROJECT		023P		045P

** REVALUATION CASE **

527 P

0290 201004694 (A) N M NAIK LITKARSH UMESH

VLSI CIRCUIT DESIGN	049	014	063P	035P
ELECTRICAL POWER SYSTEM II	059	016	075P	
ADVANCED CONTROLLED DRIVES	049	016	065P	
NEURAL NETWORKS & FUZZY LOGIC	045	016	061P	040P
FLEXIBLE AC TRANSMISSION SYSTEM	068	013	081P	034P
PROJECT		020P		040P

** REVALUATION CASE **

514 P

0291 200907852 (A) N M NAYAKOJI VISHAL MANDHAR

VLSI CIRCUIT DESIGN	057	023	080P	046P
ELECTRICAL POWER SYSTEM II	062	022	084P	
ADVANCED CONTROLLED DRIVES	057	025	082P	
NEURAL NETWORKS & FUZZY LOGIC	068	021	089P	044P
FLEXIBLE AC TRANSMISSION SYSTEM	053	017	070P	036P
PROJECT		022P		044P

** REVALUATION CASE **

597 P

06 ELECTRICAL & ELECTRONIC
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC2007-08 **

EXAM YEAR : DEC-2012

BEAT P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	DRAL	REMARKS
NO.	CD.	VI.			MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 250
PAPER DESCRIPTION					MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0292 200907833 (A) N M PARAB ADITYA RAJENDRA

VLSI CIRCUIT DESIGN	058	015	073P	038P
ELECTRICAL POWER SYSTEM II	061	016	077P	
ADVANCED CONTROLLED DRIVES	054	018	072P	
NEURAL NETWORKS & FUZZY LOGIC	072	018	090P	038P
FLEXIBLE AC TRANSMISSION SYSTEM	056	016	072P	034P
PROJECT		022P		044P

** REVALUATION CASE **

560

P

0293 200907834 (A) N M PARAB PRIYAL BHARAT

VLSI CIRCUIT DESIGN	054	015	069P	037P
ELECTRICAL POWER SYSTEM II	066	021	087P	
ADVANCED CONTROLLED DRIVES	055	010	065P	
NEURAL NETWORKS & FUZZY LOGIC	054	010	064P	038P
FLEXIBLE AC TRANSMISSION SYSTEM	056	016	072P	035P
PROJECT		023P		046P

** REVALUATION CASE **

536

P

0294 200907843 (A) N F PARULEKER AADITI LAXMIDAS

VLSI CIRCUIT DESIGN	070	015	085P	039P
ELECTRICAL POWER SYSTEM II	055	019	074P	
ADVANCED CONTROLLED DRIVES	058	016	074P	
NEURAL NETWORKS & FUZZY LOGIC	055	019	074P	040P
FLEXIBLE AC TRANSMISSION SYSTEM	053	020	073P	037P
PROJECT		021P		042P

** REVALUATION CASE **

559

P

0295 201004690 (A) N M PATIL MAHESH JAYWANT

VLSI CIRCUIT DESIGN	051	017	068P	030P
ELECTRICAL POWER SYSTEM II	070	023	093P	
ADVANCED CONTROLLED DRIVES	058	025	083P	
NEURAL NETWORKS & FUZZY LOGIC	062	017	079P	043P
FLEXIBLE AC TRANSMISSION SYSTEM	069	020	089P	040P
PROJECT		021P		041P

** REVALUATION CASE **

507

P

0296 200907893 (A) N F PG ANITA KUMARI

VLSI CIRCUIT DESIGN	061	014	075P	031P
ELECTRICAL POWER SYSTEM II	065	020	085P	
ADVANCED CONTROLLED DRIVES	059	014	073P	
NEURAL NETWORKS & FUZZY LOGIC	075	015	090P	038P
FLEXIBLE AC TRANSMISSION SYSTEM	063	015	078P	037P
PROJECT		023P		046P

** REVALUATION CASE **

576

P

C-06 ELECTRICAL & ELECTRONIC
COLLEGE : GDA COLLEGE OF ENGINEERING

** RG2007-08 **
EXAM YEAR : DEC-2012

SEAT P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD.	VI.			MAX. 100	25	125	50 OR 25	50 OR 25	
PAPER DESCRIPTION					MIN. 040		50	20 OR 10	20 OR 10	MAX. 850 MIN. 340

0297 200807793 (A) N M PRADIP THOUNADJAM

VLSI CIRCUIT DESIGN	043	010	053P	035P
ELECTRICAL POWER SYSTEM II	060	010	070P	
ADVANCED CONTROLLED DRIVES	074	014	088P	
NEURAL NETWORKS & FUZZY LOGIC	059	012	071P	036P
FLEXIBLE AC TRANSMISSION SYSTEM	062	016	078P	034P
PROJECT		024P		048P

** REVALUATION CASE **

537 P

0298 200907835 (A) N M RAHUL REVI

VLSI CIRCUIT DESIGN	043	017	060P	039P
ELECTRICAL POWER SYSTEM II	062	021	083P	
ADVANCED CONTROLLED DRIVES	067	019	086P	
NEURAL NETWORKS & FUZZY LOGIC	076	017	093P	044P
FLEXIBLE AC TRANSMISSION SYSTEM	049	015	064P	037P
PROJECT		022P		044P

** REVALUATION CASE **

572 P

0299 200908008 (A) N F RANE SNEHA MARUTI

VLSI CIRCUIT DESIGN	040	013	053P	035P
ELECTRICAL POWER SYSTEM II	071	020	091P	
ADVANCED CONTROLLED DRIVES	058	017	075P	
NEURAL NETWORKS & FUZZY LOGIC	072	012	084P	040P
FLEXIBLE AC TRANSMISSION SYSTEM	055	019	074P	040P
PROJECT		020P		041P

** REVALUATION CASE **

553 P

0300 200703758 (A) N M REGMI PRITHVIRAJ DEVIRAM

** RESULT WITHHELD - (SEM. 5 NOT PASSED) **

VLSI CIRCUIT DESIGN	022	007	029P	030E
ELECTRICAL POWER SYSTEM II	043	012	055E	
ADVANCED CONTROLLED DRIVES	040	010	050E	
NEURAL NETWORKS & FUZZY LOGIC	042	010	052E	033E
FLEXIBLE AC TRANSMISSION SYSTEM	042	009	051E	030E
PROJECT		020E		038E

** REVALUATION CASE **

0301 200809946 (A) N M SALBAONKAR NARAYAN NICHIKANT

** RESULT WITHHELD - (SEM. 6 NOT PASSED) **

VLSI CIRCUIT DESIGN	040	011	051P	037P
ELECTRICAL POWER SYSTEM II	048	012	060P	
ADVANCED CONTROLLED DRIVES	048	010	058P	
NEURAL NETWORKS & FUZZY LOGIC	047	012	059P	038P
FLEXIBLE AC TRANSMISSION SYSTEM	050	012	062P	031P
PROJECT		021P		040P

** REVALUATION CASE **

GA ELECTRICAL & ELECTRONIC
COLLEGE : GOA COLLEGE OF ENGINEERING

** R22007-08 **

EXAM YEAR : DEC-2012

BEAT P.R. NO.	ARE PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD.	VI.		MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 850
			PAPER DESCRIPTION	MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0302 200907022 (A) N F BANGOLI APORVA

VLSI CIRCUIT DESIGN	051	017	068P	037P
ELECTRICAL POWER SYSTEM II	065	019	084P	
ADVANCED CONTROLLED DRIVES	053	013	066P	
NEURAL NETWORKS & FUZZY LOGIC	075	016	091P	044P
FLEXIBLE AC TRANSMISSION SYSTEM	046	018	064P	039P
PROJECT		020P		040P

** REVALUATION CASE **

553 P

0303 200907050 (A) N F SHETGADNKAR BIDDHI RAGHODA

VLSI CIRCUIT DESIGN	065	017	082P	039P
ELECTRICAL POWER SYSTEM II	071	022	093P	
ADVANCED CONTROLLED DRIVES	049	021	070P	
NEURAL NETWORKS & FUZZY LOGIC	070	019	089P	045P
FLEXIBLE AC TRANSMISSION SYSTEM	070	023	093P	039P
PROJECT		020P		041P

** REVALUATION CASE **

611 P

0304 200908067 (A) N F SHRADDHA KATOCH

VLSI CIRCUIT DESIGN	061	015	076P	038P
ELECTRICAL POWER SYSTEM II	061	021	082P	
ADVANCED CONTROLLED DRIVES	055	010	065P	
NEURAL NETWORKS & FUZZY LOGIC	069	017	086P	042P
FLEXIBLE AC TRANSMISSION SYSTEM	060	016	076P	036P
PROJECT		021P		042P

** REVALUATION CASE **

564 P

0305 201004681 (A) N M SINARI PRITESH PRAKASH

VLSI CIRCUIT DESIGN	047	016	063P	035P
ELECTRICAL POWER SYSTEM II	066	024	090P	
ADVANCED CONTROLLED DRIVES	060	025	085P	
NEURAL NETWORKS & FUZZY LOGIC	047	019	066P	045P
FLEXIBLE AC TRANSMISSION SYSTEM	068	022	090P	038P
PROJECT		021P		042P

** REVALUATION CASE **

594 P

0306 200908080 (A) N F BINDH KUMARI ANJANA

VLSI CIRCUIT DESIGN	056	017	073P	035P
ELECTRICAL POWER SYSTEM II	063	020	083P	
ADVANCED CONTROLLED DRIVES	066	010	076P	
NEURAL NETWORKS & FUZZY LOGIC	070	017	087P	040P
FLEXIBLE AC TRANSMISSION SYSTEM	067	013	080P	033P
PROJECT		021P		040P

** REVALUATION CASE **

568 P

COLLEGE : GOA COLLEGE OF ENGINEERING

EXAM YEAR : DEC-2012

BEAT P.R. NO.	ARE PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD.	VI.		MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 250
PAPER DESCRIPTION				MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0308 200907010 (A) N F SURLAKER ADHWIN SHANTHAR

VLSI CIRCUIT DESIGN

063

023

086P

040P

ELECTRICAL POWER SYSTEM I

060

023

083P

ADVANCED CONTROLLED DRIVES

057

019

076P

NEURAL NETWORKS & FUZZY LOGIC

052

021

073P

045P

FLEXIBLE AC TRANSMISSION SYSTEM

063

021

084P

039P

PROJECT

023P

046P

** REVALUATION CASE **

595

P

READ BY: *[Signature]* CHECKED BY: *[Signature]* DECLARED ON: 22 APR 2013 ASST. REGISTRAR (C/PROJ): *[Signature]* CONTROLLER OF EXAMINATION: *[Signature]* REGISTRAR: *[Signature]*