

GOA UNIVERSITY ENGINEERING EXAMINATION SYSTEM COLLEGEWISE STUDENT RESULT REGISTER FOR VI TH SEMESTER EXAM

I 04 ELECTRONICS & TELECOMM. ] \*\* RC 2007-08 \*\*  
 COLLEGE : GOA COLLEGE OF ENGINEERING EXAM YEAR : NOV-2012

SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	SEM. 6		SEM. 5		GRAND	REMARKS
						TOTAL		TOTAL		TOTAL	
						MAX. 850		MAX. 850		MAX. 1700	
						MIN. 340		MIN. 340		MIN. 0680	
						THEORY		PRACTICAL	ORAL		
						MAX. 100		50 OR 25	50 OR 25		
							SESS	TOTAL			
							25	125			

0026	200800966	(E)	N	M	CANDOLKAR VIRESH GURUDAS						
					COMMUNICATION ENGINEERING II	040	009	049+	\$1	024+	
					PERIPHERAL DEVICES & INTERFACING	051	003	054P			
					POWER ELECTRONICS	077	010	087+			
					ANTENA & WAVE PROPAGATION	058	010	068+			
					ELECTRONIC INSTRUMENTATION	045	010	055+			
					VLSI TECHNOLOGY & DESIGN	044	009	053+		035+	
						425	\$01	P	379	\$05	C
										0804	\$06 C

0027	200907906	(E)	N	M	DESAI SACHIN SANTOSH						
					COMMUNICATION ENGINEERING II	044	010	054+		038+	
					PERIPHERAL DEVICES & INTERFACING	049	010	059+			
					POWER ELECTRONICS	042	012	054+			
					ANTENA & WAVE PROPAGATION	043	011	054P			
					ELECTRONIC INSTRUMENTATION	042	005	047+	\$3		
					VLSI TECHNOLOGY & DESIGN	043	011	054+		036+	
						396	\$03	P	436	P	0832 \$03 C

0028	200604813	(E)	N	F	DIAS BJORN SOCRATES						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					COMMUNICATION ENGINEERING II	040	005	045+	\$5	036+	
					PERIPHERAL DEVICES & INTERFACING	040	008	048+	\$2		
					POWER ELECTRONICS	041	013	054+			
					ANTENA & WAVE PROPAGATION	000	008	008A			
					ELECTRONIC INSTRUMENTATION	045	007	052+			
					VLSI TECHNOLOGY & DESIGN	045	008	053+		037+	

0029	200703488	(E)	N	M	GADNKAR RUPESH MADHU						
					COMMUNICATION ENGINEERING II	040	004	044+	\$6	022+	
					PERIPHERAL DEVICES & INTERFACING	043	003	046P	\$4		
					POWER ELECTRONICS	050	010	060+			
					ANTENA & WAVE PROPAGATION	054	010	064+			
					ELECTRONIC INSTRUMENTATION	043	006	049+	\$1		
					VLSI TECHNOLOGY & DESIGN	040	008	048+	\$2	025+	
						358	\$13	P	342	\$14	C
										0700	\$27 C

0030	201004644	(E)	N	M	MIRASHI VINAY JAGANNATH						
					COMMUNICATION ENGINEERING II	035	\$5	011	046+	\$5	044+
					PERIPHERAL DEVICES & INTERFACING	052		013	065+		
					POWER ELECTRONICS	045		019	064+		
					ANTENA & WAVE PROPAGATION	030		010	040F		
					ELECTRONIC INSTRUMENTATION	043		007	050E		
					VLSI TECHNOLOGY & DESIGN	043		015	058+		037+
						404	\$05	F	427	C	0831 \$05 F

P: PASS CLASS; F: FAILS; A: ABSENT; N: NON APPEARANCE D: FIRST CLASS WITH DISTINCTION; I: FIRST CLASS; S: SECOND CLASS;  
 C: PASSED IN COMPARTMENT; X: NOT ANSWERED SEM. 5

[ 04 ELECTRONICS & TELECOMM. ]  
COLLEGE : GOA COLLEGE OF ENGINEERING

\*\* RC 2007-08 \*\*  
EXAM YEAR : NOV-2012

SEAT P.R. NO.	NO. ARE PRO CD. VI.	SEX	NAME OF THE CANDIDATE	SEM. 6		SEM. 5		GRAND	REMARKS
				TOTAL		TOTAL		TOTAL	
				THEORY	SESS	TOTAL	PRACTICAL	ORAL	
				MAX. 100	25	125	50 OR 25	50 OR 25	

0031 200703561 (E) N M PALNI AMEY VINAYAK \*\* RESULT WITHHELD - (SEM. 5 NOT PASSED) \*\*

COMMUNICATION ENGINEERING II	046	004	050+		020+
PERIPHERAL DEVICES & INTERFACING	040	003	043F		
POWER ELECTRONICS	042	006	048+ \$2		
ANTENA & WAVE PROPAGATION	045	006	051+		
ELECTRONIC INSTRUMENTATION	049	005	054E		
VLSI TECHNOLOGY & DESIGN	055	004	059+		025+

0032 200907862 (E) N M PANSHEKAR AKSHAY ANANT

COMMUNICATION ENGINEERING II	051	009	060+		030+
PERIPHERAL DEVICES & INTERFACING	051	009	060P		
POWER ELECTRONICS	056	012	068+		
ANTENA & WAVE PROPAGATION	068	010	078+		
ELECTRONIC INSTRUMENTATION	055	005	060P		
VLSI TECHNOLOGY & DESIGN	060	016	076+		038+
			470	P	376 \$04 C 0846 \$04 C

0033 201004641 (E) N M RAMOS DALTON FRANCIS

COMMUNICATION ENGINEERING II	035	\$5 009	044+	\$6	034+
PERIPHERAL DEVICES & INTERFACING	045	007	052P		
POWER ELECTRONICS	037	\$3 016	053+	\$3	
ANTENA & WAVE PROPAGATION	036	\$4 010	046+	\$4	
ELECTRONIC INSTRUMENTATION	040	008	048+	\$2	
VLSI TECHNOLOGY & DESIGN	047	014	061+		038+
			376	\$15 P	389 \$10 C 0765 \$25 C

0034 200807665 (E) N M SHETTY ROHITH GANPATI

COMMUNICATION ENGINEERING II	040	007	047+	\$3	022+
PERIPHERAL DEVICES & INTERFACING	039	\$1 009	048+	\$2	
POWER ELECTRONICS	045	011	056+		
ANTENA & WAVE PROPAGATION	039	\$1 007	046+	\$4	
ELECTRONIC INSTRUMENTATION	037	\$3 012	049+	\$3	
VLSI TECHNOLOGY & DESIGN	040	006	046P	\$4	025+
			339	\$16 P	362 \$06 C 0701 \$22 C

0035 200801100 (E) N F SHIRVOIKAR MANISHA DNU

COMMUNICATION ENGINEERING II	041	007	048+	\$2	033+
PERIPHERAL DEVICES & INTERFACING	040	010	050+		
POWER ELECTRONICS	033	016	049F		
ANTENA & WAVE PROPAGATION	067	016	083+		
ELECTRONIC INSTRUMENTATION	047	009	056+		
VLSI TECHNOLOGY & DESIGN	043	014	057+		038+
			414	\$02 F	407 \$07 P 0821 \$09 F

READ BY: *Hay*  
 CHECKED BY: *Bmshs*  
 DECLARED ON: **20 FEB 2013**  
 ASST. REGISTRAR-E (PROF): *Springer*  
 CONTROLLER OF EXAMINATION: *Shodgingis*  
 REGISTRAR: *Shree*

P: PASS CLASS; F: FAILS; A: ABSENT; N: NON APPEARANCE D: FIRST CLASS WITH DISTINCTION; I: FIRST CLASS; S: SECOND CLASS;  
C: PASSED IN COMPARTMENT; X: NOT ANSWERED SEM. 5

