

I 06 ELECTRICAL & ELECTRONIC]
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC2007-08 **
EXAM YEAR : DEC-2012

SEAT P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD.	VI.			MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 950
				PAPER DESCRIPTION	MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0243	200800872	(A)	N	M	ALWIN NORONHA					** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
					VLSI CIRCUIT DESIGN	024	013	037F	038E	
					ELECTRICAL POWER SYSTEM II	046	011	057E		
					ADVANCED CONTROLLED DRIVES	035 #05	015	050E#05		
					NEURAL NETWORKS & FUZZY LOGIC	044	013	057E	038E	
					FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	040	014	054E	038E	
							024E		048E	

0244	200907838	(A)	N	F	ANVEKAR ANITA ANIL					
					VLSI CIRCUIT DESIGN	050	021	071P	041P	
					ELECTRICAL POWER SYSTEM II	067	020	087P		
					ADVANCED CONTROLLED DRIVES	052	022	074P		
					NEURAL NETWORKS & FUZZY LOGIC	066	018	084P	043P	
					FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	063	021	084P	037P	
							020P		040P	
										581 P

0245	200804571	(A)	N	F	ARATI GUPTA					
					VLSI CIRCUIT DESIGN	040	014	054P	037P	
					ELECTRICAL POWER SYSTEM II	060	017	077P		
					ADVANCED CONTROLLED DRIVES	044	010	054P		
					NEURAL NETWORKS & FUZZY LOGIC	080	017	097P	038P	
					FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	070	013	083P	032P	
							020P		041P	
										533 P

0246	200907846	(A)	N	M	BHANDANKAR VISHNU VISHWANATH					
					VLSI CIRCUIT DESIGN	032	012	044F	035E	
					ELECTRICAL POWER SYSTEM II	062	016	078E		
					ADVANCED CONTROLLED DRIVES	052	015	067E		
					NEURAL NETWORKS & FUZZY LOGIC	055	012	067E	043E	
					FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	056	013	069E	033E	
							023E		046E	
										505 F

0247	200607094	(A)	N	M	BHAT PRADOSH MOHAN					** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					VLSI CIRCUIT DESIGN	019	012	031F	038E	
					ELECTRICAL POWER SYSTEM II	042	007	049E #1		
					ADVANCED CONTROLLED DRIVES	008	019	027F		
					NEURAL NETWORKS & FUZZY LOGIC	027	010	037F	038E	
					FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	037 #3	012	049E #3	031E	
							022E		044E	

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** RG2007-08 **
EXAM YEAR : DEC-2012

SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
						MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 850
						MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0248	200907831	(A)	N	M	BHUTKI GAURAV DILIP						** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
					VLSI CIRCUIT DESIGN	041	011	052P		035P	
					ELECTRICAL POWER SYSTEM II	064	018	082P			
					ADVANCED CONTROLLED DRIVES	040	010	050P			
					NEURAL NETWORKS & FUZZY LOGIC	050	008	058P		035P	
					FLEXIBLE AC TRANSMISSION SYSTEM	047	012	059P		033P	
					PROJECT		022P			045P	

0249	200703655	(A)	N	M	CHAGAS PEREIRA JASON MARK STEFAN						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					VLSI CIRCUIT DESIGN	018	010	028F		030E	
					ELECTRICAL POWER SYSTEM II	038	#2 011	049E #2			
					ADVANCED CONTROLLED DRIVES	026	010	036F			
					NEURAL NETWORKS & FUZZY LOGIC	025	012	037F		030E	
					FLEXIBLE AC TRANSMISSION SYSTEM	035	007	042F		033E	
					PROJECT		020E			038E	

0250	200907812	(A)	N	M	CHARI DEVENDRA DEVANAND						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					VLSI CIRCUIT DESIGN	027	013	040F		030E	
					ELECTRICAL POWER SYSTEM II	046	011	057E			
					ADVANCED CONTROLLED DRIVES	042	010	052E			
					NEURAL NETWORKS & FUZZY LOGIC	044	013	057E		038E	
					FLEXIBLE AC TRANSMISSION SYSTEM	047	012	059E		032E	
					PROJECT		021E			042E	

0251	200907855	(A)	N	F	CHARI KAUSTUBH SURESH						
					VLSI CIRCUIT DESIGN	040	014	054P		045P	
					ELECTRICAL POWER SYSTEM II	065	017	082P			
					ADVANCED CONTROLLED DRIVES	041	010	051P			
					NEURAL NETWORKS & FUZZY LOGIC	055	017	072P		043P	
					FLEXIBLE AC TRANSMISSION SYSTEM	050	013	063P		034P	
					PROJECT		023P			045P	

512 P

0252	200904051	(A)	N	M	CHAVAN ANIRUDHA CHANDRASHEKHAR						
					VLSI CIRCUIT DESIGN	045	012	057P		045P	
					ELECTRICAL POWER SYSTEM II	070	023	093P			
					ADVANCED CONTROLLED DRIVES	052	010	062P			
					NEURAL NETWORKS & FUZZY LOGIC	067	010	077P		040P	
					FLEXIBLE AC TRANSMISSION SYSTEM	078	022	100P		037P	
					PROJECT		023P			046P	

580 P

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SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD.	VI.				MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 850
		PAPER DESCRIPTION				MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0253	200907856	(A)	N	M	CHINCHREKAR SANDESH BHASKAR						** RESULT WITHHELD FOR MALPRACTICE **
					VLSI CIRCUIT DESIGN						
					ELECTRICAL POWER SYSTEM II						
					ADVANCED CONTROLLED DRIVES						
					NEURAL NETWORKS & FUZZY LOGIC						
					FLEXIBLE AC TRANSMISSION SYSTEM						
					PROJECT						

0254	200907857	(A)	N	M	CHODANKAR VIGNESH UDAY						
					VLSI CIRCUIT DESIGN	067	020	087P		039P	
					ELECTRICAL POWER SYSTEM II	067	019	086P			
					ADVANCED CONTROLLED DRIVES	055	021	076P			
					NEURAL NETWORKS & FUZZY LOGIC	076	018	094P		045P	
					FLEXIBLE AC TRANSMISSION SYSTEM	066	021	087P		039P	
					PROJECT		023P			046P	

622 P

0255	200801101	(A)	N	M	DA COSTA EMANUEL ANTONIO SIMAO						
					VLSI CIRCUIT DESIGN	030	017	047F		031E	
					ELECTRICAL POWER SYSTEM II	061	020	081E			
					ADVANCED CONTROLLED DRIVES	048	025	073E			
					NEURAL NETWORKS & FUZZY LOGIC	065	017	082E		043E	
					FLEXIBLE AC TRANSMISSION SYSTEM	054	019	073E		038E	
					PROJECT		024E			048E	

540 F

0256	200607085	(A)	N	M	DHARGALKAR SUDHIR GOVIND						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					VLSI CIRCUIT DESIGN	026	012	038F		034E	
					ELECTRICAL POWER SYSTEM II	054	015	069E			
					ADVANCED CONTROLLED DRIVES	020	010	030F			
					NEURAL NETWORKS & FUZZY LOGIC	040	012	052E		036E	
					FLEXIBLE AC TRANSMISSION SYSTEM	046	015	061E		033E	
					PROJECT		021E			042E	

0257	200907847	(A)	N	F	DIAS MARIA FLEUR DOMINICA						
					VLSI CIRCUIT DESIGN	051	020	071P		041P	
					ELECTRICAL POWER SYSTEM II	068	021	089P			
					ADVANCED CONTROLLED DRIVES	061	025	086P			
					NEURAL NETWORKS & FUZZY LOGIC	086	025	111P		044P	
					FLEXIBLE AC TRANSMISSION SYSTEM	071	020	091P		039P	
					PROJECT		024P			048P	

644 P

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SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	GRAL	REMARKS
NO.	ED. VI.					MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 850
	PAPER DESCRIPTION					MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0258	200907829	(A)	N	F	FERNANDES ALLISTON SANTOS						
	VLSI CIRCUIT DESIGN					046	015	061P		038P	
	ELECTRICAL POWER SYSTEM II					065	020	085P			
	ADVANCED CONTROLLED DRIVES					048	021	069P			
	NEURAL NETWORKS & FUZZY LOGIC					071	016	087P		042P	
	FLEXIBLE AC TRANSMISSION SYSTEM					059	015	074P		036P	
	PROJECT						020P			041P	
											553 P

0259	200907836	(A)	N	M	FERNANDO JENSEN						
	VLSI CIRCUIT DESIGN					060	015	075P		036P	
	ELECTRICAL POWER SYSTEM II					060	019	079P			
	ADVANCED CONTROLLED DRIVES					040	016	056P			
	NEURAL NETWORKS & FUZZY LOGIC					062	017	079P		044P	
	FLEXIBLE AC TRANSMISSION SYSTEM					044	014	058P		035P	
	PROJECT						021P			042P	
											525 P

0260	200908070	(A)	N	M	GANGMEI GAISHINTHUI						
	VLSI CIRCUIT DESIGN					045	013	058P		038P	
	ELECTRICAL POWER SYSTEM II					063	014	077P			
	ADVANCED CONTROLLED DRIVES					044	014	058P			
	NEURAL NETWORKS & FUZZY LOGIC					059	013	072P		039P	
	FLEXIBLE AC TRANSMISSION SYSTEM					051	018	069P		038P	
	PROJECT						024P			048P	
											521 #10 P

0261	200908005	(A)	N	F	GAUNS DESAI ASHWINI ABHAY						
	VLSI CIRCUIT DESIGN					040	017	057P		038P	
	ELECTRICAL POWER SYSTEM II					063	019	082P			
	ADVANCED CONTROLLED DRIVES					036	#4 016	052P #4			
	NEURAL NETWORKS & FUZZY LOGIC					050	016	066P		039P	
	FLEXIBLE AC TRANSMISSION SYSTEM					062	014	076P		035P	
	PROJECT						022P			044P	
											511 #04 P

0262	200907845	(A)	N	M	GAVAS VIJESH GANESH						
	VLSI CIRCUIT DESIGN					040	010	050P		033P	
	ELECTRICAL POWER SYSTEM II					063	018	081P			
	ADVANCED CONTROLLED DRIVES					042	010	052P			
	NEURAL NETWORKS & FUZZY LOGIC					066	010	076P		038P	
	FLEXIBLE AC TRANSMISSION SYSTEM					061	011	072P		033P	
	PROJECT						022P			044P	
											501 P

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SEAT P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	ED.	VI.			MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 850
	PAPER DESCRIPTION				MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0263	200703675	(A)	N	M	GHADI AMIT TANAPPA					** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					VLSI CIRCUIT DESIGN	031	005	036F	030E	
					ELECTRICAL POWER SYSTEM II	062	019	081E		
					ADVANCED CONTROLLED DRIVES	030	010	040F		
					NEURAL NETWORKS & FUZZY LOGIC	053	012	065E	040E	
					FLEXIBLE AC TRANSMISSION SYSTEM	037	#3 011	048E #3	030E	
					PROJECT		022E		042E	

0264	200907825	(A)	N	M	GHANCHI SURESHKUMAR LAKMARAM					
					VLSI CIRCUIT DESIGN	040	015	055E	038E	
					ELECTRICAL POWER SYSTEM II	063	017	080E		
					ADVANCED CONTROLLED DRIVES	030	022	052F		
					NEURAL NETWORKS & FUZZY LOGIC	046	016	062E	040E	
					FLEXIBLE AC TRANSMISSION SYSTEM	043	014	057E	042E	
					PROJECT		024E		048E	

498 F

0265	200907842	(A)	N	F	GODINHO AELREA GENOVEVA					
					VLSI CIRCUIT DESIGN	040	016	056P	038P	
					ELECTRICAL POWER SYSTEM II	071	018	089P		
					ADVANCED CONTROLLED DRIVES	066	021	087P		
					NEURAL NETWORKS & FUZZY LOGIC	070	019	089P	043P	
					FLEXIBLE AC TRANSMISSION SYSTEM	066	014	080P	041P	
					PROJECT		024P		048P	

595 P

0266	200907832	(A)	N	M	GOVENKAR SWETAN SURYAKANT					
					VLSI CIRCUIT DESIGN	042	021	063P	038P	
					ELECTRICAL POWER SYSTEM II	068	020	088P		
					ADVANCED CONTROLLED DRIVES	049	025	074P		
					NEURAL NETWORKS & FUZZY LOGIC	070	022	092P	044P	
					FLEXIBLE AC TRANSMISSION SYSTEM	066	020	086P	040P	
					PROJECT		022P		043P	

590 P

0267	200907840	(A)	N	M	JADHAV VARUN PRATAPSIKHA					** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
					VLSI CIRCUIT DESIGN	023	012	035F	035E	
					ELECTRICAL POWER SYSTEM II	052	017	069E		
					ADVANCED CONTROLLED DRIVES	040	010	050E		
					NEURAL NETWORKS & FUZZY LOGIC	055	008	063E	038E	
					FLEXIBLE AC TRANSMISSION SYSTEM	057	015	072E	036E	
					PROJECT		019E		039E	

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SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
						MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 050
						MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0268	200907804	(A)	N	M	JALMI SIDDHANT KASHINATH						
					VLSI CIRCUIT DESIGN	042	017	059P		035P	
					ELECTRICAL POWER SYSTEM II	063	016	079P			
					ADVANCED CONTROLLED DRIVES	036 #04	021	057P#04			
					NEURAL NETWORKS & FUZZY LOGIC	051	017	068P		041P	
					FLEXIBLE AC TRANSMISSION SYSTEM	070	020	090P		039P	
					PROJECT		024P			048P	
											540 #12 P

0269	200907837	(A)	N	M	KADNEKAR VINAYAK ALIAS SIDDHANT PANDURANG						** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
					VLSI CIRCUIT DESIGN	042	005	047E #3		030E	
					ELECTRICAL POWER SYSTEM II	053	013	066E			
					ADVANCED CONTROLLED DRIVES	024	010	034F			
					NEURAL NETWORKS & FUZZY LOGIC	040	004	044E #6		028E	
					FLEXIBLE AC TRANSMISSION SYSTEM	035	004	039F		030E	
					PROJECT		018E			036E	

0270	200907826	(A)	N	F	KALANGUTKAR MEGHNA VIJAY						
					VLSI CIRCUIT DESIGN	045	020	065P		045P	
					ELECTRICAL POWER SYSTEM II	068	020	088P			
					ADVANCED CONTROLLED DRIVES	058	020	078P			
					NEURAL NETWORKS & FUZZY LOGIC	071	017	088P		042P	
					FLEXIBLE AC TRANSMISSION SYSTEM	061	018	079P		039P	
					PROJECT		020P			041P	
											585 P

0271	200907805	(A)	N	F	KAMAT PRAGATI PRABHAKAR						
					VLSI CIRCUIT DESIGN	059	018	077E		032E	
					ELECTRICAL POWER SYSTEM II	072	012	084E			
					ADVANCED CONTROLLED DRIVES	030	016	046F			
					NEURAL NETWORKS & FUZZY LOGIC	052	016	068E		040E	
					FLEXIBLE AC TRANSMISSION SYSTEM	054	016	070E		036E	
					PROJECT		022E			044E	
											519 F

0272	200801182	(A)	N	M	KERKAR SIDDHESH SURYA						
					VLSI CIRCUIT DESIGN	044	016	060P		030P	
					ELECTRICAL POWER SYSTEM II	056	021	077P			
					ADVANCED CONTROLLED DRIVES	045	010	055P			
					NEURAL NETWORKS & FUZZY LOGIC	047	014	061P		038P	
					FLEXIBLE AC TRANSMISSION SYSTEM	050	018	068P		034P	
					PROJECT		020P			038P	
											481 P

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SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD.	VI.				MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 850
	PAPER DESCRIPTION					MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0273 200907827 (A) N F KHANDEPARKAR SHWETA MAHENDRA

VLSI CIRCUIT DESIGN	045	016	061P	044P
ELECTRICAL POWER SYSTEM II	062	021	083P	
ADVANCED CONTROLLED DRIVES	056	021	077P	
NEURAL NETWORKS & FUZZY LOGIC	066	016	082P	043P
FLEXIBLE AC TRANSMISSION SYSTEM	050	015	065P	039P
PROJECT		022P		042P

558 P

0274 200908068 (A) N M M PRAKASH REDDY

VLSI CIRCUIT DESIGN	050	020	070P	045P
ELECTRICAL POWER SYSTEM II	069	019	088P	
ADVANCED CONTROLLED DRIVES	048	025	073P	
NEURAL NETWORKS & FUZZY LOGIC	087	023	110P	043P
FLEXIBLE AC TRANSMISSION SYSTEM	091	022	113P	045P
PROJECT		020P		041P

648 P

0275 200908065 (A) N F MANISHA KAUSHIK

VLSI CIRCUIT DESIGN	046	016	062P	030P
ELECTRICAL POWER SYSTEM II	060	012	072P	
ADVANCED CONTROLLED DRIVES	038	#2 010	048P #2	
NEURAL NETWORKS & FUZZY LOGIC	064	014	078P	040P
FLEXIBLE AC TRANSMISSION SYSTEM	054	012	066P	038P
PROJECT		022P		044P

500 #02 P

0276 200907854 (A) N F MARATHE SWETA VIVEK

VLSI CIRCUIT DESIGN	030 #07#3	021	051P#07#3	045P
ELECTRICAL POWER SYSTEM II	063	022	085P	
ADVANCED CONTROLLED DRIVES	030 #05#5	018	048P#05#5	
NEURAL NETWORKS & FUZZY LOGIC	068	021	089P	044P
FLEXIBLE AC TRANSMISSION SYSTEM	048	017	065P	040P
PROJECT		024P		048P

537 #12#08 P

0277 200907848 (A) N M MASKERI NABRAJ GOPAL

VLSI CIRCUIT DESIGN	041	022	063P	042P
ELECTRICAL POWER SYSTEM II	070	021	091P	
ADVANCED CONTROLLED DRIVES	042	024	066P	
NEURAL NETWORKS & FUZZY LOGIC	084	024	108P	044P
FLEXIBLE AC TRANSMISSION SYSTEM	057	021	078P	041P
PROJECT		023P		044P

600 P

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SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
						MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 850
					PAPER DESCRIPTION	MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0278	200801055	(A)	N	F	MHAMAL MEGHA MOHAN						
					VLSI CIRCUIT DESIGN	045	014	059P		030P	
					ELECTRICAL POWER SYSTEM II	064	021	085P			
					ADVANCED CONTROLLED DRIVES	049	021	070P			
					NEURAL NETWORKS & FUZZY LOGIC	060	015	075P		035P	
					FLEXIBLE AC TRANSMISSION SYSTEM	067	021	088P		038P	
					PROJECT		022P			043P	
											545 P

0279	201004685	(A)	N	M	MOHAMMED AFZAL ABULKASIM MULLA						** RESULT WITHHELD (SEM. 5 NOT PASSED) **
					VLSI CIRCUIT DESIGN	025	006	031F		030E	
					ELECTRICAL POWER SYSTEM II	050	013	063E			
					ADVANCED CONTROLLED DRIVES	030	010	040F			
					NEURAL NETWORKS & FUZZY LOGIC	042	005	047E	43	030E	
					FLEXIBLE AC TRANSMISSION SYSTEM	038	005	043F		033E	
					PROJECT		020E			038E	

0280	200907841	(A)	N	M	NADKARNI KALPEET PRASAD						
					VLSI CIRCUIT DESIGN	042	017	059P		042P	
					ELECTRICAL POWER SYSTEM II	067	019	086P			
					ADVANCED CONTROLLED DRIVES	050	021	071P			
					NEURAL NETWORKS & FUZZY LOGIC	051	014	065P		040P	
					FLEXIBLE AC TRANSMISSION SYSTEM	059	014	073P		039P	
					PROJECT		023P			045P	
											543 P

0281	201004698	(A)	N	M	NAIK CHETAN TULSIDAS						
					VLSI CIRCUIT DESIGN	042	014	056E		038E	
					ELECTRICAL POWER SYSTEM II	062	017	079E			
					ADVANCED CONTROLLED DRIVES	030	010	040F			
					NEURAL NETWORKS & FUZZY LOGIC	063	013	076E		042E	
					FLEXIBLE AC TRANSMISSION SYSTEM	050	017	067E		034E	
					PROJECT		020E			038E	
											490 F

0282	200907821	(A)	N	F	NAIK DISHA DILEEP						
					VLSI CIRCUIT DESIGN	042	018	060P		039P	
					ELECTRICAL POWER SYSTEM II	062	023	085P			
					ADVANCED CONTROLLED DRIVES	055	015	070P			
					NEURAL NETWORKS & FUZZY LOGIC	068	015	083P		040P	
					FLEXIBLE AC TRANSMISSION SYSTEM	061	018	079P		034P	
					PROJECT		022P			044P	
											556 P

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SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
						MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 350
						MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0283 200807636 (A) N M NAIK GAURESH NARAHARI ** RESULT WITHHELD - (SEM. 5 NOT PASSED) **

VLSI CIRCUIT DESIGN	040	015	055E	037E
ELECTRICAL POWER SYSTEM II	067	019	086E	
ADVANCED CONTROLLED DRIVES	030	020	050F	
NEURAL NETWORKS & FUZZY LOGIC	040	013	053E	038E
FLEXIBLE AC TRANSMISSION SYSTEM	053	016	069E	032E
PROJECT		022E		042E

0284 200907844 (A) N F NAIK NAVTI SHRADHA VISHNU

VLSI CIRCUIT DESIGN	042	022	064P	045P
ELECTRICAL POWER SYSTEM II	067	022	089P	
ADVANCED CONTROLLED DRIVES	060	021	081P	
NEURAL NETWORKS & FUZZY LOGIC	078	023	101P	044P
FLEXIBLE AC TRANSMISSION SYSTEM	067	020	087P	039P
PROJECT		023P		046P

619 P

0285 200703731 (A) N M NAIK RAJDEEP ROHIDAS

VLSI CIRCUIT DESIGN	032	013	045F	035E
ELECTRICAL POWER SYSTEM II	070	014	084E	
ADVANCED CONTROLLED DRIVES	036	\$4 014	050E \$4	
NEURAL NETWORKS & FUZZY LOGIC	042	013	055E	038E
FLEXIBLE AC TRANSMISSION SYSTEM	044	016	060E	034E
PROJECT		022E		041E

464 \$04 F

0286 200907809 (A) N F NAIK SANJIVANI BHARAT

VLSI CIRCUIT DESIGN	050	025	075P	047P
ELECTRICAL POWER SYSTEM II	073	022	095P	
ADVANCED CONTROLLED DRIVES	066	025	091P	
NEURAL NETWORKS & FUZZY LOGIC	087	025	112P	047P
FLEXIBLE AC TRANSMISSION SYSTEM	069	022	091P	044P
PROJECT		023P		047P

672 P

0287 200907818 (A) N M NAIK SARANG SHAMBHU

VLSI CIRCUIT DESIGN	031	012	043F	041E
ELECTRICAL POWER SYSTEM II	064	017	081E	
ADVANCED CONTROLLED DRIVES	048	014	062E	
NEURAL NETWORKS & FUZZY LOGIC	063	014	077E	040E
FLEXIBLE AC TRANSMISSION SYSTEM	051	018	069E	037E
PROJECT		023E		045E

518 F

E-06 ELECTRICAL & ELECTRONIC
COLLEGE : GDA COLLEGE OF ENGINEERING

** RC2007-08 **
EXAM YEAR : DEC-2012

SEAT P.R. NO.	ARE PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD.	VI.		MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 850
PAPER DESCRIPTION				MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0288	200907811	(A)	N	F	NAIK BIDDHI DNYANESHWAR					
	VLSI CIRCUIT DESIGN				052		024		076P	045P
	ELECTRICAL POWER SYSTEM II				066		025		091P	
	ADVANCED CONTROLLED DRIVES				064		020		084P	
	NEURAL NETWORKS & FUZZY LOGIC				071		022		093P	044P
	FLEXIBLE AC TRANSMISSION SYSTEM				074		022		096P	041P
	PROJECT						023P			046P
									639	P

0289	200907815	(A)	N	F	NAIK SONIA AJIT					
	VLSI CIRCUIT DESIGN				050		012		062E	038E
	ELECTRICAL POWER SYSTEM II				065		013		078E	
	ADVANCED CONTROLLED DRIVES				030		016		046F	
	NEURAL NETWORKS & FUZZY LOGIC				066		017		083E	044E
	FLEXIBLE AC TRANSMISSION SYSTEM				043		015		058E	034E
	PROJECT						023E			045E
									511	F

0290	201004694	(A)	N	M	NAIK UTKARSH UMESH				** RESULT WITHHELD - (SEM. 6 NOT PASSED) **	
	VLSI CIRCUIT DESIGN				031		014		045F	035E
	ELECTRICAL POWER SYSTEM II				059		016		075E	
	ADVANCED CONTROLLED DRIVES				032		016		048F	
	NEURAL NETWORKS & FUZZY LOGIC				045		016		061E	040E
	FLEXIBLE AC TRANSMISSION SYSTEM				068		013		081E	034E
	PROJECT						020E			040E

0291	200907852	(A)	N	M	NAYAKOJI VISHAL MANOHAR					
	VLSI CIRCUIT DESIGN				041		023		064P	046P
	ELECTRICAL POWER SYSTEM II				062		022		084P	
	ADVANCED CONTROLLED DRIVES				046		025		071P	
	NEURAL NETWORKS & FUZZY LOGIC				068		021		089P	044P
	FLEXIBLE AC TRANSMISSION SYSTEM				053		017		070P	036P
	PROJECT						022P			044P
									570	P

0292	200907833	(A)	N	M	PARAB ADITYA RAJENDRA					
	VLSI CIRCUIT DESIGN				040		015		055P	038P
	ELECTRICAL POWER SYSTEM II				061		016		077P	
	ADVANCED CONTROLLED DRIVES				040		018		058P	
	NEURAL NETWORKS & FUZZY LOGIC				072		018		090P	038P
	FLEXIBLE AC TRANSMISSION SYSTEM				056		016		072P	034P
	PROJECT						022P			044P
									528	P

C 04 ELECTRICAL & ELECTRONIC
COLLEGE : GDA COLLEGE OF ENGINEERING

** RC2007-08 **
EXAM YEAR : DEC-2012

SEAT P.R. NO.	ARE PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD.	VI.		MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 850
			PAPER DESCRIPTION	MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0293	200907834	(A)	N	M	PARAB PRIYAL BHARAT					
					VLSI CIRCUIT DESIGN	040	015	055P	037P	
					ELECTRICAL POWER SYSTEM II	066	021	087P		
					ADVANCED CONTROLLED DRIVES	040	010	050P		
					NEURAL NETWORKS & FUZZY LOGIC	054	010	064P	038P	
					FLEXIBLE AC TRANSMISSION SYSTEM	056	016	072P	035P	
					PROJECT		023P	046P		
									507	P

0294	200907843	(A)	N	F	PARULEKER AADITI LAXMIDAS				** RESULT WITHHELD - (SEM. 6 NOT PASSED) **	
					VLSI CIRCUIT DESIGN	052	015	067P	039P	
					ELECTRICAL POWER SYSTEM II	055	019	074P		
					ADVANCED CONTROLLED DRIVES	058	016	074P		
					NEURAL NETWORKS & FUZZY LOGIC	055	019	074P	040P	
					FLEXIBLE AC TRANSMISSION SYSTEM	053	020	073P	037P	
					PROJECT		021P	042P		

0295	201004690	(A)	N	M	PATIL MAHESH JAYWANT					
					VLSI CIRCUIT DESIGN	050	017	067P	030P	
					ELECTRICAL POWER SYSTEM II	070	023	093P		
					ADVANCED CONTROLLED DRIVES	038	*2	025	063P *2	
					NEURAL NETWORKS & FUZZY LOGIC	062	017	079P	043P	
					FLEXIBLE AC TRANSMISSION SYSTEM	069	020	089P	040P	
					PROJECT		021P	041P		
									566	*02 P

0296	200907823	(A)	N	F	PG ANITA KUMARI					
					VLSI CIRCUIT DESIGN	044	014	058P	031P	
					ELECTRICAL POWER SYSTEM II	065	020	085P		
					ADVANCED CONTROLLED DRIVES	044	014	058P		
					NEURAL NETWORKS & FUZZY LOGIC	075	015	090P	038P	
					FLEXIBLE AC TRANSMISSION SYSTEM	063	015	078P	037P	
					PROJECT		023P	046P		
									544	P

0297	200807793	(A)	N	M	PRADIP THOUNADJAM					
					VLSI CIRCUIT DESIGN	041	010	051P	035P	
					ELECTRICAL POWER SYSTEM II	060	010	070P		
					ADVANCED CONTROLLED DRIVES	057	014	071P		
					NEURAL NETWORKS & FUZZY LOGIC	047	012	059P	036P	
					FLEXIBLE AC TRANSMISSION SYSTEM	062	016	078P	034P	
					PROJECT		024P	048P		
									506	P

E-04 ELECTRICAL & ELECTRONIC
COLLEGE : GDA COLLEGE OF ENGINEERING

** RC2007-08 **
EXAM YEAR : DEC-2012

SEAT P.R. NO.	ARE PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	GD.	VI.		MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 050
PAPER DESCRIPTION				MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0298	200907835	(A)	N	M	RAHUL REVI				
					VLSI CIRCUIT DESIGN	027	017	044F	039E
					ELECTRICAL POWER SYSTEM II	062	021	083E	
					ADVANCED CONTROLLED DRIVES	054	019	073E	
					NEURAL NETWORKS & FUZZY LOGIC	076	017	093E	044E
					FLEXIBLE AC TRANSMISSION SYSTEM	049	015	064E	037E
					PROJECT		022E		044E
									543 F

0299	200908008	(A)	N	F	RANE SNEHA MARUTI				
					VLSI CIRCUIT DESIGN	040	013	053E	035E
					ELECTRICAL POWER SYSTEM II	071	020	091E	
					ADVANCED CONTROLLED DRIVES	034	017	051F	
					NEURAL NETWORKS & FUZZY LOGIC	072	012	084E	040E
					FLEXIBLE AC TRANSMISSION SYSTEM	055	019	074E	040E
					PROJECT		020E		041E
									529 F

0300	200703758	(A)	N	M	REGMI PRITHVIRAJ DEVIRAM				** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					VLSI CIRCUIT DESIGN	021	007	028F	032E
					ELECTRICAL POWER SYSTEM II	043	012	055E	
					ADVANCED CONTROLLED DRIVES	023	010	033F	
					NEURAL NETWORKS & FUZZY LOGIC	042	010	052E	033E
					FLEXIBLE AC TRANSMISSION SYSTEM	042	009	051E	032E
					PROJECT		020E		038E

0301	200800946	(A)	N	M	SALGAONKAR NARAYAN NICHIKANT				** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
					VLSI CIRCUIT DESIGN	027	011	038F	037E
					ELECTRICAL POWER SYSTEM II	048	012	060E	
					ADVANCED CONTROLLED DRIVES	030	010	040F	
					NEURAL NETWORKS & FUZZY LOGIC	047	012	059E	038E
					FLEXIBLE AC TRANSMISSION SYSTEM	050	012	062E	031E
					PROJECT		021E		040E

0302	200907822	(A)	N	F	SANKOLI APDORVA				
					VLSI CIRCUIT DESIGN	042	017	059P	037P
					ELECTRICAL POWER SYSTEM II	065	019	084P	
					ADVANCED CONTROLLED DRIVES	042	013	055P	
					NEURAL NETWORKS & FUZZY LOGIC	075	016	091P	044P
					FLEXIBLE AC TRANSMISSION SYSTEM	046	018	064P	039P
					PROJECT		020P		040P
									533 P

[06 ELECTRICAL & ELECTRONIC]
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC2007-08 **
EXAM YEAR : DEC-2012

SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
						MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 850
						MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0303	200907050	(A)	N	F	SHETGADNKAR BIDDHI RAGHOBA						
					VLSI CIRCUIT DESIGN	046	017	063P		039P	
					ELECTRICAL POWER SYSTEM II	071	022	093P			
					ADVANCED CONTROLLED DRIVES	049	021	070P			
					NEURAL NETWORKS & FUZZY LOGIC	070	019	089P		045P	
					FLEXIBLE AC TRANSMISSION SYSTEM	070	023	093P		039P	
					PROJECT		020P			041P	
											592 P

0304	200908067	(A)	N	F	SHRADDHA KATOCH						
					VLSI CIRCUIT DESIGN	042	015	057P		038P	
					ELECTRICAL POWER SYSTEM II	061	021	082P			
					ADVANCED CONTROLLED DRIVES	042	010	052P			
					NEURAL NETWORKS & FUZZY LOGIC	069	017	086P		042P	
					FLEXIBLE AC TRANSMISSION SYSTEM	060	016	076P		036P	
					PROJECT		021P			042P	
											532 P

0305	201004681	(A)	N	M	SINARI PRITESH PRAKASH						
					VLSI CIRCUIT DESIGN	043	016	059P		035P	
					ELECTRICAL POWER SYSTEM II	066	024	090P			
					ADVANCED CONTROLLED DRIVES	046	025	071P			
					NEURAL NETWORKS & FUZZY LOGIC	067	018	085P		045P	
					FLEXIBLE AC TRANSMISSION SYSTEM	068	022	090P		038P	
					PROJECT		021P			042P	
											576 P

0306	200908080	(A)	N	F	SINGH KUMARI ANJANA						
					VLSI CIRCUIT DESIGN	040	017	057P		035P	
					ELECTRICAL POWER SYSTEM II	063	020	083P			
					ADVANCED CONTROLLED DRIVES	048	010	058P			
					NEURAL NETWORKS & FUZZY LOGIC	070	017	087P		040P	
					FLEXIBLE AC TRANSMISSION SYSTEM	067	013	080P		033P	
					PROJECT		021P			040P	
											534 P

0307	200908091	(A)	N	M	SUBRAT SURYAKANT KAMAT						
					VLSI CIRCUIT DESIGN	042	017	059P		042P	
					ELECTRICAL POWER SYSTEM II	070	020	090P			
					ADVANCED CONTROLLED DRIVES	044	017	061P			
					NEURAL NETWORKS & FUZZY LOGIC	071	017	088P		040P	
					FLEXIBLE AC TRANSMISSION SYSTEM	060	018	078P		037P	
					PROJECT		022P			044P	
											561 P

E 04 ELECTRICAL & ELECTRONIC
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC2007-08 **
EXAM YEAR : DEC-2012

SEAT P.R. NO.	ARE PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD.	VI.		MAX. 100	25	125	50 OR 25	50 OR 25	MAX. 850
	PAPER DESCRIPTION			MIN. 040		50	20 OR 10	20 OR 10	MIN. 340

0308	200907810	(A)	N	F	SURLAKER ASHWIN SHARIHAR				
					VLSI CIRCUIT DESIGN	041	023	064P	040P
					ELECTRICAL POWER SYSTEM II	060	023	083P	
					ADVANCED CONTROLLED DRIVES	038 #2	019	057P #2	
					NEURAL NETWORKS & FUZZY LOGIC	052	021	073P	045P
					FLEXIBLE AC TRANSMISSION SYSTEM	063	021	084P	039P
					PROJECT		023P		046P
									554 #02 P

0309	200907858	(A)	N	M	VIRINGAR KARAN SHYAM				** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
					VLSI CIRCUIT DESIGN	030	006	036F	030E
					ELECTRICAL POWER SYSTEM II	056	010	066E	
					ADVANCED CONTROLLED DRIVES	030 #10	010	040E#10	
					NEURAL NETWORKS & FUZZY LOGIC	024	007	031F	028E
					FLEXIBLE AC TRANSMISSION SYSTEM	046	007	053E	030E
					PROJECT		020E		040E

Ray
READ BY

B. Mendonca
CHECKED BY

20 FEB 2013
DECLARED ON

[Signature]
20/2/2013
ASST. REGISTRAR - E (PROF)

[Signature]
CONTROLLER OF EXAMINATION

[Signature]
REGISTRAR