



**Result Register Report**  
Bachelor of Engineering(RC19-20) CBCS

**College Name:** Government of Goa College of Engineering

**Branch Name:** Electronics (VLSI Design & Technology)

**Examination :** Semester V November 2024

1 **Seat No:** 50812 **PR Number:** 202211635 **Name:** ADITEE ABHAY KAMAT

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	56 P	6	14	76 P			76	B	6	24
VLSI 520	Analog VLSI Design	4	62 P	7	13	82 P			82	B	6	24
VLSI 531	Computer Organization and Design	3	64 P	14		78 P			78	B+	7	21
VLSI 541	Soft Computing	3	32 F	11		43 F			43	F	0	0
VLSI 550	Digital VLSI Design Lab	1			15 P	15 P	15P		30	B+	7	7
VLSI 560	Analog VLSI Design Lab	1			16 P	16 P	24P		40	A+	9	9
VLSI 570	HDL Lab	2			36 P	36 P	20P		56	A	8	16
HM009	Ethics and Entrepreneurship	3	72 P	14		86 P			86	A	8	24
ME535	Industrial Safety and Occupational Health	3	80 P	18		98 P			98	A+	9	27

**Total Weighted Grade Points :** 152

**Entitlement Marks :** # 10

**Earned Credits:** 21

**SGPA :** --

2 **Seat No:** 50813 **PR Number:** 202211433 **Name:** AKSHAY SANJEEV KULKARNI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	47 P	19	20	86 P			86	B+	7	28
VLSI 520	Analog VLSI Design	4	74 P	21	22	117 P			117	A+	9	36
VLSI 531	Computer Organization and Design	3	50 P	25		75 P			75	B+	7	21
VLSI 541	Soft Computing	3	70 P	21		91 P			91	A	8	24
VLSI 550	Digital VLSI Design Lab	1			21 P	21 P	25P		46	O	10	10
VLSI 560	Analog VLSI Design Lab	1			23 P	23 P	23P		46	O	10	10
VLSI 570	HDL Lab	2			48 P	48 P	25P		73	O	10	20
ET544	Electronic Instrumentation and Automation	3	62 P	16		78 P			78	B+	7	21
HM009	Ethics and Entrepreneurship	3	55 P	20		75 P			75	B+	7	21
@IOT-02	Introduction to Security of Cyber-Physical Systems	4	40 P	21		61 P	22P		83	B+	7	28

**Total Weighted Grade Points :** 191

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 7.96

3 **Seat No:** 50814 **PR Number:** 202211322 **Name:** ANKIT RAKESH MISHRA

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	51 P	12	11	74 P			74	C	5	20
VLSI 520	Analog VLSI Design	4	71 P	8	12	91 P			91	B+	7	28
VLSI 531	Computer Organization and Design	3	58 P	19		77 P			77	B+	7	21
VLSI 541	Soft Computing	3	63 P	12		75 P			75	B+	7	21
VLSI 550	Digital VLSI Design Lab	1			18 P	18 P	17P		35	A	8	8
VLSI 560	Analog VLSI Design Lab	1			20 P	20 P	22P		42	A+	9	9
VLSI 570	HDL Lab	2			38 P	38 P	20P		58	A+	9	18
CV543	Green Building	3	67 P	19		86 P			86	A	8	24
HM009	Ethics and Entrepreneurship	3	55 P	12		67 P			67	B	6	18

**Total Weighted Grade Points :** 167

**Entitlement Marks :** # 10

**Earned Credits:** 24

**SGPA :** 6.96

4 **Seat No:** 50815 **PR Number:** 202211429 **Name:** ARYA ANIL PARAB

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	51 P	10	20	81 P			81	B	6	24
VLSI 520	Analog VLSI Design	4	68 P	13	24	105 P			105	A	8	32
VLSI 531	Computer Organization and Design	3	59 P	19		78 P			78	B+	7	21
VLSI 541	Soft Computing	3	72 P	22		94 P			94	A+	9	27
VLSI 550	Digital VLSI Design Lab	1			25 P	25 P	24P		49	O	10	10
VLSI 560	Analog VLSI Design Lab	1			24 P	24 P	22P		46	O	10	10
VLSI 570	HDL Lab	2			48 P	48 P	25P		73	O	10	20
HM009	Ethics and Entrepreneurship	3	51 P	16		67 P			67	B	6	18
IT534	Internet of Things	3	66 P	13		79 P			79	B+	7	21

**Total Weighted Grade Points :** 183

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 7.63



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**College Name:** Government of Goa College of Engineering  
**Branch Name:** Electronics (VLSI Design & Technology)  
**Examination :** Semester V November 2024

**5 Seat No: 50816 PR Number: 202211953 Name: ARYA DNYANESHWAR BIRJE**

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	21 F	8	15	44 F			44	F	0	0
VLSI 520	Analog VLSI Design	4	66 P	6	17	89 P			89	B+	7	28
VLSI 531	Computer Organization and Design	3	55 P	15		70 P			70	B+	7	21
VLSI 541	Soft Computing	3	43 P	11		54 P			54	P	4	12
VLSI 550	Digital VLSI Design Lab	1			15 P	15 P	10P		25	B	6	6
VLSI 560	Analog VLSI Design Lab	1			19 P	19 P	23P		42	A+	9	9
VLSI 570	HDL Lab	2			36 P	36 P	18P		54	A	8	16
CV543	Green Building	3	65 P	14		79 P			79	B+	7	21
HM009	Ethics and Entrepreneurship	3	40 P	15		55 P			55	P	4	12

**Total Weighted Grade Points : 125**

**Entitlement Marks : --**

**Earned Credits: 20**

**SGPA : --**

**6 Seat No: 50817 PR Number: 202211636 Name: ARYA RAJESH BHATKANDE**

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	59 P	9	15	83 P			83	B+	7	28
VLSI 520	Analog VLSI Design	4	75 P	9	13	97 P			97	B+	7	28
VLSI 531	Computer Organization and Design	3	43 P	13		56 P			56	P	4	12
VLSI 541	Soft Computing	3	45 P	12		57 P			57	C	5	15
VLSI 550	Digital VLSI Design Lab	1			16 P	16 P	22P		38	A+	9	9
VLSI 560	Analog VLSI Design Lab	1			18 P	18 P	22P		40	A+	9	9
VLSI 570	HDL Lab	2			36 P	36 P	20P		56	A	8	16
HM009	Ethics and Entrepreneurship	3	48 P	12		60 P			60	C	5	15
IT534	Internet of Things	3	49 P	8		57 P			57	C	5	15

**Total Weighted Grade Points : 147**

**Entitlement Marks : --**

**Earned Credits: 24**

**SGPA : 6.13**

**7 Seat No: 50818 PR Number: 202211391 Name: ARYAN KAUSHAL MISHRA**

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	64 P	11	16	91 P			91	B+	7	28
VLSI 520	Analog VLSI Design	4	90 P	10	15	115 P			115	A+	9	36
VLSI 531	Computer Organization and Design	3	53 P	21		74 P			74	B+	7	21
VLSI 541	Soft Computing	3	78 P	16		94 P			94	A+	9	27
VLSI 550	Digital VLSI Design Lab	1			19 P	19 P	18P		37	A	8	8
VLSI 560	Analog VLSI Design Lab	1			22 P	22 P	22P		44	O	10	10
VLSI 570	HDL Lab	2			38 P	38 P	25P		63	A+	9	18
CV543	Green Building	3	67 P	17		84 P			84	A	8	24
HM009	Ethics and Entrepreneurship	3	67 P	13		80 P			80	B+	7	21

**Total Weighted Grade Points : 193**

**Entitlement Marks : --**

**Earned Credits: 24**

**SGPA : 8.04**

**8 Seat No: 50819 PR Number: 202211694 Name: ASHWIN NARESH MALIK**

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	51 P	9	15	75 P			75	B	6	24
VLSI 520	Analog VLSI Design	4	72 P	10	18	100 P			100	A	8	32
VLSI 531	Computer Organization and Design	3	66 P	12		78 P			78	B+	7	21
VLSI 541	Soft Computing	3	54 P	10		64 P			64	B	6	18
VLSI 550	Digital VLSI Design Lab	1			10 P	10 P	18P		28	B+	7	7
VLSI 560	Analog VLSI Design Lab	1			17 P	17 P	22P		39	A+	9	9
VLSI 570	HDL Lab	2			42 P	42 P	16P		58	A+	9	18
HM009	Ethics and Entrepreneurship	3	47 P	8		55 P			55	P	4	12
IT534	Internet of Things	3	40 P	7		47 F			47	F	0	0

**Total Weighted Grade Points : 141**

**Entitlement Marks : --**

**Earned Credits: 21**

**SGPA : --**



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**College Name:** Government of Goa College of Engineering

**Branch Name:** Electronics (VLSI Design & Technology)

**Examination :** Semester V November 2024

9 **Seat No:** 50820 **PR Number:** 202211637 **Name:** DHANARAJ NITESH MULVI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	42 P	6	14	62 P			62	P	4	16
VLSI 520	Analog VLSI Design	4	50 P	5	15	70 P			70	C	5	20
VLSI 531	Computer Organization and Design	3	48 P	11		59 P			59	C	5	15
VLSI 541	Soft Computing	3	60 P	10		70 P			70	B+	7	21
VLSI 550	Digital VLSI Design Lab	1			18 P	18 P	10P		28	B+	7	7
VLSI 560	Analog VLSI Design Lab	1			19 P	19 P	22P		41	A+	9	9
VLSI 570	HDL Lab	2			37 P	37 P	17P		54	A	8	16
CV543	Green Building	3	51 P	12		63 P			63	B	6	18
HM009	Ethics and Entrepreneurship	3	51 P	11		62 P			62	C	5	15

**Total Weighted Grade Points :** 137

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 5.71

10 **Seat No:** 50821 **PR Number:** 202211430 **Name:** DIVYA NAGESH PRABHU

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	70 P	24	24	118 P			118	A+	9	36
VLSI 520	Analog VLSI Design	4	85 P	24	24	133 P			133	O	10	40
VLSI 531	Computer Organization and Design	3	75 P	25		100 P			100	A+	9	27
VLSI 541	Soft Computing	3	83 P	24		107 P			107	O	10	30
VLSI 550	Digital VLSI Design Lab	1			22 P	22 P	25P		47	O	10	10
VLSI 560	Analog VLSI Design Lab	1			22 P	22 P	25P		47	O	10	10
VLSI 570	HDL Lab	2			48 P	48 P	25P		73	O	10	20
ET544	Electronic Instrumentation and Automation	3	75 P	21		96 P			96	A+	9	27
HM009	Ethics and Entrepreneurship	3	84 P	23		107 P			107	O	10	30
@CEAM-02	Introduction to Artificial Intelligence & Machine Learning	4	61 P	18		79 P		18 P	97	B+	7	28

**Total Weighted Grade Points :** 230

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 9.58

11 **Seat No:** 50822 **PR Number:** 202211610 **Name:** DIVYA SIROBHUSHANAM

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	71 P	11	18	100 P			100	A	8	32
VLSI 520	Analog VLSI Design	4	74 P	15	16	105 P			105	A	8	32
VLSI 531	Computer Organization and Design	3	69 P	16		85 P			85	A	8	24
VLSI 541	Soft Computing	3	88 P	14		102 P			102	A+	9	27
VLSI 550	Digital VLSI Design Lab	1			19 P	19 P	18P		37	A	8	8
VLSI 560	Analog VLSI Design Lab	1			21 P	21 P	25P		46	O	10	10
VLSI 570	HDL Lab	2			43 P	43 P	16P		59	A+	9	18
HM009	Ethics and Entrepreneurship	3	74 P	17		91 P			91	A	8	24
IT534	Internet of Things	3	72 P	11		83 P			83	A	8	24

**Total Weighted Grade Points :** 199

**Entitlement Marks :** # 10

**Earned Credits:** 24

**SGPA :** 8.29

12 **Seat No:** 50823 **PR Number:** 202211306 **Name:** DOBAASHISH DEV

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	51 P	11	20	82 P			82	B	6	24
VLSI 520	Analog VLSI Design	4	71 P	5	15	91 P			91	B+	7	28
VLSI 531	Computer Organization and Design	3	64 P	11		75 P			75	B+	7	21
VLSI 541	Soft Computing	3	48 P	11		59 P			59	C	5	15
VLSI 550	Digital VLSI Design Lab	1			15 P	15 P	23P		38	A+	9	9
VLSI 560	Analog VLSI Design Lab	1			19 P	19 P	18P		37	A	8	8
VLSI 570	HDL Lab	2			36 P	36 P	17P		53	A	8	16
HM009	Ethics and Entrepreneurship	3	59 P	13		72 P			72	B+	7	21
IT534	Internet of Things	3	44 P	6		50 P			50	P	4	12

**Total Weighted Grade Points :** 154

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 6.42



Result Register Report
Bachelor of Engineering(RC19-20) CBCS

College Name: Government of Goa College of Engineering
Branch Name: Electronics (VLSI Design & Technology)
Examination : Semester V November 2024

13 Seat No: 50824 PR Number: 202211938 Name: GILBILE VEDANT LAXMAN

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510-570, CV535, and HM009.

Total Weighted Grade Points : 158 Entitlement Marks : -- Earned Credits: 24 SGPA : 6.58

14 Seat No: 50825 PR Number: 202211517 Name: GREESHMA HARISH NAYAK

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510-570, HM009, and IT534.

Total Weighted Grade Points : 173 Entitlement Marks : -- Earned Credits: 24 SGPA : 7.21

15 Seat No: 50826 PR Number: 202211663 Name: HARISH HANUMANT NAIK

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510-570, CV543, and HM009.

Total Weighted Grade Points : 190 Entitlement Marks : -- Earned Credits: 24 SGPA : 7.92

16 Seat No: 50827 PR Number: 202211494 Name: JOHAN JOAQUIM FERNANDES

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510-570, HM009, and IT534.

Total Weighted Grade Points : 157 Entitlement Marks : -- Earned Credits: 20 SGPA : --



**Result Register Report**  
Bachelor of Engineering(RC19-20) CBCS

**College Name:** Government of Goa College of Engineering

**Branch Name:** Electronics (VLSI Design & Technology)

**Examination :** Semester V November 2024

**17 Seat No:** 50828 **PR Number:** 202211453 **Name:** KANISHKA KIRAN MANDREKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	42 P	9	18	69 P			69	C	5	20
VLSI 520	Analog VLSI Design	4	78 P	15	18	111 P			111	A	8	32
VLSI 531	Computer Organization and Design	3	65 P	16		81 P			81	B+	7	21
VLSI 541	Soft Computing	3	74 P	15		89 P			89	A	8	24
VLSI 550	Digital VLSI Design Lab	1			23 P	23 P	21P		44	O	10	10
VLSI 560	Analog VLSI Design Lab	1			20 P	20 P	22P		42	A+	9	9
VLSI 570	HDL Lab	2			30 P	30 P	22P		52	A	8	16
CV532	Numerical methods	3	48 P	13		61 P			61	C	5	15
HM009	Ethics and Entrepreneurship	3	51 P	8		59 P			59	C	5	15

**Total Weighted Grade Points :** 162

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 6.75

**18 Seat No:** 50829 **PR Number:** 202311062 **Name:** KUNDAIKER MADHUKER NANU

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	42 P	12	17	71 P			71	C	5	20
VLSI 520	Analog VLSI Design	4	72 P	8	18	98 P			98	A	8	32
VLSI 531	Computer Organization and Design	3	40 P	14		54 P			54	P	4	12
VLSI 541	Soft Computing	3	54 P	12		66 P			66	B	6	18
VLSI 550	Digital VLSI Design Lab	1			25 P	25 P	18P		43	O	10	10
VLSI 560	Analog VLSI Design Lab	1			20 P	20 P	24P		44	O	10	10
VLSI 570	HDL Lab	2			40 P	40 P	21P		61	A+	9	18
ET544	Electronic Instrumentation and Automation	3	64 P	10		74 P			74	B+	7	21
HM009	Ethics and Entrepreneurship	3	43 P	12		55 P			55	P	4	12

**Total Weighted Grade Points :** 153

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 6.38

**19 Seat No:** 50830 **PR Number:** 202211518 **Name:** MALLIKARJUN BASAPPA GAUD

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	42 P	12	15	69 P			69	C	5	20
VLSI 520	Analog VLSI Design	4	54 P	10	16	80 P			80	B	6	24
VLSI 531	Computer Organization and Design	3	52 P	14		66 P			66	B	6	18
VLSI 541	Soft Computing	3	57 P	13		70 P			70	B+	7	21
VLSI 550	Digital VLSI Design Lab	1			19 P	19 P	15P		34	A	8	8
VLSI 560	Analog VLSI Design Lab	1			20 P	20 P	21P		41	A+	9	9
VLSI 570	HDL Lab	2			34 P	34 P	20P		54	A	8	16
CV543	Green Building	3	60 P	17		77 P			77	B+	7	21
HM009	Ethics and Entrepreneurship	3	24 F	14		38 F			38	F	0	0

**Total Weighted Grade Points :** 137

**Entitlement Marks :** --

**Earned Credits:** 21

**SGPA :** --

**20 Seat No:** 50831 **PR Number:** 202211638 **Name:** MALVIKA GAJANAN KORGAONKER

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	56 P	18	18	92 P			92	B+	7	28
VLSI 520	Analog VLSI Design	4	80 P	17	20	117 P			117	A+	9	36
VLSI 531	Computer Organization and Design	3	63 P	17		80 P			80	B+	7	21
VLSI 541	Soft Computing	3	73 P	17		90 P			90	A	8	24
VLSI 550	Digital VLSI Design Lab	1			20 P	20 P	18P		38	A+	9	9
VLSI 560	Analog VLSI Design Lab	1			23 P	23 P	22P		45	O	10	10
VLSI 570	HDL Lab	2			46 P	46 P	22P		68	O	10	20
ET544	Electronic Instrumentation and Automation	3	70 P	17		87 P			87	A	8	24
HM009	Ethics and Entrepreneurship	3	60 P	20		80 P			80	B+	7	21

**Total Weighted Grade Points :** 193

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 8.04



**Result Register Report**  
Bachelor of Engineering(RC19-20) CBCS

**College Name:** Government of Goa College of Engineering

**Branch Name:** Electronics (VLSI Design & Technology)

**Examination :** Semester V November 2024

21 **Seat No:** 50832 **PR Number:** 202311061 **Name:** MANDREKAR BALKRISHANA SHASHIKANT

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	37 F	10	18	65 F			65	F	0	0
VLSI 520	Analog VLSI Design	4	68 P	12	20	100 P			100	A	8	32
VLSI 531	Computer Organization and Design	3	53 P	13		66 P			66	B	6	18
VLSI 541	Soft Computing	3	66 P	11		77 P			77	B+	7	21
VLSI 550	Digital VLSI Design Lab	1			25 P	25 P	21P		46	O	10	10
VLSI 560	Analog VLSI Design Lab	1			19 P	19 P	21P		40	A+	9	9
VLSI 570	HDL Lab	2			44 P	44 P	22P		66	O	10	20
CE542	Web-Technologies	3	42 P	11		53 P			53	P	4	12
HM009	Ethics and Entrepreneurship	3	43 P	15		58 P			58	C	5	15

**Total Weighted Grade Points :** 137

**Entitlement Marks :** --

**Earned Credits:** 20

**SGPA :** --

22 **Seat No:** 50833 **PR Number:** 202211238 **Name:** MARC ROBERT

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	17 F	2	18	37 F			37	F	0	0
VLSI 520	Analog VLSI Design	4	17 F	4	16	37 F			37	F	0	0
VLSI 531	Computer Organization and Design	3	40 P	9		49 F			49	F	0	0
VLSI 541	Soft Computing	3	40 P	10		50 P			50	P	4	12
VLSI 550	Digital VLSI Design Lab	1			21 P	21 P	20P		41	A+	9	9
VLSI 560	Analog VLSI Design Lab	1			22 P	22 P	17P		39	A+	9	9
VLSI 570	HDL Lab	2			47 P	47 P	22P		69	O	10	20
HM009	Ethics and Entrepreneurship	3	55 P	19		74 P			74	B+	7	21
IT534	Internet of Things	3	52 P	10		62 P			62	C	5	15

**Total Weighted Grade Points :** 86

**Entitlement Marks :** --

**Earned Credits:** 13

**SGPA :** --

23 **Seat No:** 50834 **PR Number:** 202211293 **Name:** MITHILA HARSHAD VELINGKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	40 P	14	12	66 P			66	P	4	16
VLSI 520	Analog VLSI Design	4	49 P	8	15	72 P			72	C	5	20
VLSI 531	Computer Organization and Design	3	54 P	19		73 P			73	B+	7	21
VLSI 541	Soft Computing	3	52 P	10		62 P			62	C	5	15
VLSI 550	Digital VLSI Design Lab	1			18 P	18 P	21P		39	A+	9	9
VLSI 560	Analog VLSI Design Lab	1			21 P	21 P	20P		41	A+	9	9
VLSI 570	HDL Lab	2			46 P	46 P	18P		64	O	10	20
HM009	Ethics and Entrepreneurship	3	57 P	17		74 P			74	B+	7	21
IT534	Internet of Things	3	42 P	11		53 P			53	P	4	12

**Total Weighted Grade Points :** 143

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 5.96

24 **Seat No:** 50835 **PR Number:** 202211519 **Name:** NARAYAN SHANKER SAWANT

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	42 P	11	14	67 P			67	P	4	16
VLSI 520	Analog VLSI Design	4	67 P	7	13	87 P			87	B+	7	28
VLSI 531	Computer Organization and Design	3	41 P	13		54 P			54	P	4	12
VLSI 541	Soft Computing	3	63 P	16		79 P			79	B+	7	21
VLSI 550	Digital VLSI Design Lab	1			20 P	20 P	19P		39	A+	9	9
VLSI 560	Analog VLSI Design Lab	1			22 P	22 P	22P		44	O	10	10
VLSI 570	HDL Lab	2			40 P	40 P	22P		62	A+	9	18
EE532	Illumination Engineering	3	41 P	13		54 P			54	P	4	12
HM009	Ethics and Entrepreneurship	3	54 P	11		65 P			65	B	6	18

**Total Weighted Grade Points :** 144

**Entitlement Marks :** # 10

**Earned Credits:** 24

**SGPA :** 6



**Result Register Report**  
Bachelor of Engineering(RC19-20) CBCS

**College Name:** Government of Goa College of Engineering  
**Branch Name:** Electronics (VLSI Design & Technology)  
**Examination :** Semester V November 2024

25 **Seat No:** 50836 **PR Number:** 202309925 **Name:** NIGEL MENDES

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	56 P	3	17	76 P			76	B	6	24
VLSI 520	Analog VLSI Design	4	70 P	8	12	90 P			90	B+	7	28
VLSI 531	Computer Organization and Design	3	44 P	14		58 P			58	C	5	15
VLSI 541	Soft Computing	3	46 P	10		56 P			56	P	4	12
VLSI 550	Digital VLSI Design Lab	1			15 P	15 P	16P		31	B+	7	7
VLSI 560	Analog VLSI Design Lab	1			13 P	13 P	15P		28	B+	7	7
VLSI 570	HDL Lab	2			24 P	24 P	17P		41	B	6	12
ET544	Electronic Instrumentation and Automation	3	54 P	10		64 P			64	B	6	18
HM009	Ethics and Entrepreneurship	3	59 P	10		69 P			69	B+	7	21

**Total Weighted Grade Points :** 144

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 6

26 **Seat No:** 50837 **PR Number:** 202211432 **Name:** PRASAD BALAJI ST TOKARI KOLI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	74 P	17	18	109 P			109	A	8	32
VLSI 520	Analog VLSI Design	4	84 P	19	20	123 P			123	A+	9	36
VLSI 531	Computer Organization and Design	3	69 P	21		90 P			90	A	8	24
VLSI 541	Soft Computing	3	62 P	18		80 P			80	B+	7	21
VLSI 550	Digital VLSI Design Lab	1			21 P	21 P	20P		41	A+	9	9
VLSI 560	Analog VLSI Design Lab	1			23 P	23 P	22P		45	O	10	10
VLSI 570	HDL Lab	2			46 P	46 P	21P		67	O	10	20
ET544	Electronic Instrumentation and Automation	3	73 P	14		87 P			87	A	8	24
HM009	Ethics and Entrepreneurship	3	60 P	18		78 P			78	B+	7	21
@IOT-02	Introduction to Security of Cyber-Physical Systems	4	56 P	22		78 P	18P		96	B+	7	28

**Total Weighted Grade Points :** 197

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 8.21

27 **Seat No:** 50838 **PR Number:** 202309926 **Name:** PRASHANTH RAJESH MARATHE

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	43 P	10	10	63 P			63	P	4	16
VLSI 520	Analog VLSI Design	4	75 P	6	14	95 P			95	B+	7	28
VLSI 531	Computer Organization and Design	3	50 P	11		61 P			61	C	5	15
VLSI 541	Soft Computing	3	28 F	13		41 F			41	F	0	0
VLSI 550	Digital VLSI Design Lab	1			15 P	15 P	12P		27	B	6	6
VLSI 560	Analog VLSI Design Lab	1			18 P	18 P	22P		40	A+	9	9
VLSI 570	HDL Lab	2			27 P	27 P	18P		45	B+	7	14
ET544	Electronic Instrumentation and Automation	3	53 P	14		67 P			67	B	6	18
HM009	Ethics and Entrepreneurship	3	40 P	10		50 P			50	P	4	12

**Total Weighted Grade Points :** 118

**Entitlement Marks :** --

**Earned Credits:** 21

**SGPA :** --

28 **Seat No:** 50839 **PR Number:** 202211567 **Name:** PRERANA RAJENDRA GHADI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	67 P	20	23	110 P			110	A	8	32
VLSI 520	Analog VLSI Design	4	75 P	19	24	118 P			118	A+	9	36
VLSI 531	Computer Organization and Design	3	69 P	21		90 P			90	A	8	24
VLSI 541	Soft Computing	3	78 P	20		98 P			98	A+	9	27
VLSI 550	Digital VLSI Design Lab	1			24 P	24 P	20P		44	O	10	10
VLSI 560	Analog VLSI Design Lab	1			20 P	20 P	23P		43	O	10	10
VLSI 570	HDL Lab	2			46 P	46 P	24P		70	O	10	20
CV535	Occupational Safety and Health	3	71 P	21		92 P			92	A	8	24
HM009	Ethics and Entrepreneurship	3	67 P	14		81 P			81	B+	7	21

**Total Weighted Grade Points :** 204

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 8.5



Result Register Report
Bachelor of Engineering(RC19-20) CBCS

College Name: Government of Goa College of Engineering
Branch Name: Electronics (VLSI Design & Technology)
Examination : Semester V November 2024

29 Seat No: 50840 PR Number: 202211392 Name: RIYA P M

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510, VLSI 520, VLSI 531, VLSI 541, VLSI 550, VLSI 560, VLSI 570, ET544, HM009.

Total Weighted Grade Points : 167 Entitlement Marks : # 10 Earned Credits: 24 SGPA : 6.96

30 Seat No: 50841 PR Number: 202211283 Name: ROHAN S JOSEPH

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510, VLSI 520, VLSI 531, VLSI 541, VLSI 550, VLSI 560, VLSI 570, HM009, IT534.

Total Weighted Grade Points : 122 Entitlement Marks : -- Earned Credits: 20 SGPA : --

31 Seat No: 50842 PR Number: 202211520 Name: RUDRESH RAJARAM RAUT

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510, VLSI 520, VLSI 531, VLSI 541, VLSI 550, VLSI 560, VLSI 570, CV543, HM009.

Total Weighted Grade Points : 132 Entitlement Marks : -- Earned Credits: 21 SGPA : --

32 Seat No: 50843 PR Number: 202211695 Name: RUTAMBAR DILIP DESAI

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510, VLSI 520, VLSI 531, VLSI 541, VLSI 550, VLSI 560, VLSI 570, CV543, HM009.

Total Weighted Grade Points : 108 Entitlement Marks : # 10 Earned Credits: 16 SGPA : --





**Result Register Report**  
Bachelor of Engineering(RC19-20) CBCS

**College Name:** Government of Goa College of Engineering

**Branch Name:** Electronics (VLSI Design & Technology)

**Examination :** Semester V November 2024

**33 Seat No:** 50844 **PR Number:** 202211393 **Name:** SAHIL LAXIMAN DHAWASKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	57 P	10	15	82 P			82	B	6	24
VLSI 520	Analog VLSI Design	4	74 P	15	12	101 P			101	A	8	32
VLSI 531	Computer Organization and Design	3	62 P	11		73 P			73	B+	7	21
VLSI 541	Soft Computing	3	80 P	16		96 P			96	A+	9	27
VLSI 550	Digital VLSI Design Lab	1			23 P	23 P	17P		40	A+	9	9
VLSI 560	Analog VLSI Design Lab	1			19 P	19 P	24P		43	O	10	10
VLSI 570	HDL Lab	2			43 P	43 P	20P		63	A+	9	18
CV543	Green Building	3	72 P	16		88 P			88	A	8	24
HM009	Ethics and Entrepreneurship	3	61 P	17		78 P			78	B+	7	21

**Total Weighted Grade Points :** 186

**Entitlement Marks :** # 10

**Earned Credits:** 24

**SGPA :** 7.75

**34 Seat No:** 50845 **PR Number:** 202211431 **Name:** SALIL HARISHCHANDRA NAIK

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	34 F	11	15	60 F			60	F	0	0
VLSI 520	Analog VLSI Design	4	67 P	4	16	87 P			87	B+	7	28
VLSI 531	Computer Organization and Design	3	40 P	13		53 P			53	P	4	12
VLSI 541	Soft Computing	3	51 P	10		61 P			61	C	5	15
VLSI 550	Digital VLSI Design Lab	1			19 P	19 P	19P		38	A+	9	9
VLSI 560	Analog VLSI Design Lab	1			18 P	18 P	23P		41	A+	9	9
VLSI 570	HDL Lab	2			38 P	38 P	21P		59	A+	9	18
CV543	Green Building	3	45 P	14		59 P			59	C	5	15
HM009	Ethics and Entrepreneurship	3	52 P	10		62 P			62	C	5	15

**Total Weighted Grade Points :** 121

**Entitlement Marks :** # 10

**Earned Credits:** 20

**SGPA :** --

**35 Seat No:** 50846 **PR Number:** 202211281 **Name:** SAMRUDHI SACHIN REGE

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	41 P	10	14	65 P			65	P	4	16
VLSI 520	Analog VLSI Design	4	65 P	11	18	94 P			94	B+	7	28
VLSI 531	Computer Organization and Design	3	69 P	16		85 P			85	A	8	24
VLSI 541	Soft Computing	3	55 P	20		75 P			75	B+	7	21
VLSI 550	Digital VLSI Design Lab	1			25 P	25 P	18P		43	O	10	10
VLSI 560	Analog VLSI Design Lab	1			22 P	22 P	24P		46	O	10	10
VLSI 570	HDL Lab	2			45 P	45 P	24P		69	O	10	20
CV543	Green Building	3	53 P	19		72 P			72	B+	7	21
HM009	Ethics and Entrepreneurship	3	52 P	13		65 P			65	B	6	18

**Total Weighted Grade Points :** 168

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 7

**36 Seat No:** 50847 **PR Number:** 202211639 **Name:** SHAYYUB NABISAB SHAIKH SANDHI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	47 P	7	14	68 P			68	C	5	20
VLSI 520	Analog VLSI Design	4	40 P	7	14	61 P			61	P	4	16
VLSI 531	Computer Organization and Design	3	47 P	13		60 P			60	C	5	15
VLSI 541	Soft Computing	3	45 P	10		55 P			55	P	4	12
VLSI 550	Digital VLSI Design Lab	1			18 P	18 P	15P		33	A	8	8
VLSI 560	Analog VLSI Design Lab	1			18 P	18 P	22P		40	A+	9	9
VLSI 570	HDL Lab	2			32 P	32 P	21P		53	A	8	16
CV535	Occupational Safety and Health	3	59 P	18		77 P			77	B+	7	21
HM009	Ethics and Entrepreneurship	3	62 P	13		75 P			75	B+	7	21

**Total Weighted Grade Points :** 138

**Entitlement Marks :** --

**Earned Credits:** 24

**SGPA :** 5.75



**Result Register Report**  
Bachelor of Engineering(RC19-20) CBCS

**College Name:** Government of Goa College of Engineering  
**Branch Name:** Electronics (VLSI Design & Technology)  
**Examination :** Semester V November 2024

37 **Seat No:** 50848 **PR Number:** 202211955 **Name:** SHERLEINE ALIZA GODINHO

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	47 P	7	13	67 P			67	P	4	16
VLSI 520	Analog VLSI Design	4	62 P	4	16	82 P			82	B	6	24
VLSI 531	Computer Organization and Design	3	62 P	12		74 P			74	B+	7	21
VLSI 541	Soft Computing	3	40 P	12		52 P			52	P	4	12
VLSI 550	Digital VLSI Design Lab	1			18 P	18 P	19P		37	A	8	8
VLSI 560	Analog VLSI Design Lab	1			13 P	13 P	23P		36	A	8	8
VLSI 570	HDL Lab	2			35 P	35 P	21P		56	A	8	16
CE542	Web-Technologies	3	47 P	17		64 P			64	B	6	18
HM009	Ethics and Entrepreneurship	3	51 P	17		68 P			68	B	6	18

**Total Weighted Grade Points :** 141

**Entitlement Marks : --**

**Earned Credits:** 24

**SGPA :** 5.88

38 **Seat No:** 50849 **PR Number:** 202309927 **Name:** SHIVANAND PRAKASH SASNOOR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	41 P	2	18	61 P			61	P	4	16
VLSI 520	Analog VLSI Design	4	60 P	7	13	80 P			80	B	6	24
VLSI 531	Computer Organization and Design	3	51 P	13		64 P			64	B	6	18
VLSI 541	Soft Computing	3	40 P	12		52 P			52	P	4	12
VLSI 550	Digital VLSI Design Lab	1			17 P	17 P	20P		37	A	8	8
VLSI 560	Analog VLSI Design Lab	1			19 P	19 P	23P		42	A+	9	9
VLSI 570	HDL Lab	2			30 P	30 P	22P		52	A	8	16
ET544	Electronic Instrumentation and Automation	3	58 P	10		68 P			68	B	6	18
HM009	Ethics and Entrepreneurship	3	47 P	11		58 P			58	C	5	15

**Total Weighted Grade Points :** 136

**Entitlement Marks : --**

**Earned Credits:** 24

**SGPA :** 5.67

39 **Seat No:** 50850 **PR Number:** 202211696 **Name:** SHIVDATT SUDAN SHENVI KUNCOLIENKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	51 P	14	14	79 P			79	B	6	24
VLSI 520	Analog VLSI Design	4	64 P	3	17	84 P			84	B+	7	28
VLSI 531	Computer Organization and Design	3	66 P	20		86 P			86	A	8	24
VLSI 541	Soft Computing	3	64 P	17		81 P			81	B+	7	21
VLSI 550	Digital VLSI Design Lab	1			22 P	22 P	22P		44	O	10	10
VLSI 560	Analog VLSI Design Lab	1			19 P	19 P	22P		41	A+	9	9
VLSI 570	HDL Lab	2			39 P	39 P	25P		64	O	10	20
HM009	Ethics and Entrepreneurship	3	62 P	17		79 P			79	B+	7	21
IT534	Internet of Things	3	64 P	10		74 P			74	B+	7	21

**Total Weighted Grade Points :** 178

**Entitlement Marks : --**

**Earned Credits:** 24

**SGPA :** 7.42

40 **Seat No:** 50851 **PR Number:** 202211615 **Name:** SHREYA CHANDRAKANT KHOKARALKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	40 P	12	17	69 P			69	C	5	20
VLSI 520	Analog VLSI Design	4	70 P	15	17	102 P			102	A	8	32
VLSI 531	Computer Organization and Design	3	56 P	10		66 P			66	B	6	18
VLSI 541	Soft Computing	3	40 P	10		50 P			50	P	4	12
VLSI 550	Digital VLSI Design Lab	1			16 P	16 P	18P		34	A	8	8
VLSI 560	Analog VLSI Design Lab	1			17 P	17 P	20P		37	A	8	8
VLSI 570	HDL Lab	2			36 P	36 P	24P		60	A+	9	18
HM009	Ethics and Entrepreneurship	3	44 P	13		57 P			57	C	5	15
IT534	Internet of Things	3	52 P	10		62 P			62	C	5	15

**Total Weighted Grade Points :** 146

**Entitlement Marks : --**

**Earned Credits:** 24

**SGPA :** 6.08



Result Register Report
Bachelor of Engineering(RC19-20) CBCS

College Name: Government of Goa College of Engineering
Branch Name: Electronics (VLSI Design & Technology)
Examination : Semester V November 2024

41 Seat No: 50852 PR Number: 202211282 Name: SHREYASH PRAVIN P USGAONKAR

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510-570, CV543, and HM009.

Total Weighted Grade Points : 206 Entitlement Marks : -- Earned Credits: 24 SGPA : 8.58

42 Seat No: 50853 PR Number: 202211954 Name: SHVETALI ALIAS SAKSHI TANAJI THENGIL

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510-570, ET544, and HM009.

Total Weighted Grade Points : 165 Entitlement Marks : -- Earned Credits: 24 SGPA : 6.88

43 Seat No: 50854 PR Number: 202211496 Name: SIDHANT VINOD PATEL

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510-570, CV543, and HM009.

Total Weighted Grade Points : 145 Entitlement Marks : # 10 Earned Credits: 24 SGPA : 6.04

44 Seat No: 50855 PR Number: 202311066 Name: SUGNESH NAGUESH NAIK

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510-570, ET544, and HM009.

Total Weighted Grade Points : 132 Entitlement Marks : -- Earned Credits: 24 SGPA : 5.5



Result Register Report
Bachelor of Engineering(RC19-20) CBCS

College Name: Government of Goa College of Engineering

Branch Name: Electronics (VLSI Design & Technology)

Examination : Semester V November 2024

45 Seat No: 50856 PR Number: 202311067 Name: SUYASH NAIK

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510-570, ET544, and HM009.

Total Weighted Grade Points : 152 Entitlement Marks : -- Earned Credits: 24 SGPA : 6.33

46 Seat No: 50857 PR Number: 202211497 Name: TEJAS SUDAN MAYEKAR

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510-570, CV543, and HM009.

Total Weighted Grade Points : 197 Entitlement Marks : -- Earned Credits: 24 SGPA : 8.21

47 Seat No: 50858 PR Number: 202211413 Name: VAIDEHI GOURISH DABOLKAR

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510-570, CE542, and HM009.

Total Weighted Grade Points : 180 Entitlement Marks : # 10 Earned Credits: 24 SGPA : 7.5

48 Seat No: 50859 PR Number: 202211412 Name: VAISHNAVI KRISHNANAND NAIK

Table with 13 columns: Paper Code, Paper Name, Paper Credits, Theory, IA, Term Work, Theory Total, Practical, Oral, Total, Grade, Grade Point, Weighted Grade Points. Rows include VLSI 510-570, CV543, and HM009.

Total Weighted Grade Points : 173 Entitlement Marks : -- Earned Credits: 24 SGPA : 7.21



# Goa University

Taleigao Plateau, Goa - 403206

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## Result Register Report Bachelor of Engineering(RC19-20) CBCS

College Name: Government of Goa College of Engineering

Branch Name: Electronics (VLSI Design & Technology)

Examination : Semester V November 2024

49 Seat No: 50860 PR Number: 202211274 Name: VARAD RAJENDRA PEDNEKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 510	Digital VLSI Design	4	69 P	20	18	107 P			107	A	8	32
VLSI 520	Analog VLSI Design	4	78 P	12	17	107 P			107	A	8	32
VLSI 531	Computer Organization and Design	3	71 P	16		87 P			87	A	8	24
VLSI 541	Soft Computing	3	70 P	14		84 P			84	A	8	24
VLSI 550	Digital VLSI Design Lab	1			23 P	23 P	25P		48	O	10	10
VLSI 560	Analog VLSI Design Lab	1			21 P	21 P	24P		45	O	10	10
VLSI 570	HDL Lab	2			34 P	34 P	25P		59	A+	9	18
CE542	Web-Technologies	3	55 P	19		74 P			74	B+	7	21
HM009	Ethics and Entrepreneurship	3	78 P	21		99 P			99	A+	9	27

Total Weighted Grade Points : 198

Entitlement Marks : --

Earned Credits: 24

SGPA : 8.25

Read By:   
Checked By: 

Date : 07 FEB 2025

  
Asstt. Registrar Exam(Prof)

  
Asstt. Registrar Exam

  
Controller of Examinations