



Revaluation Result Register Report Bachelor of Engineering(RC19-20) CBCS

College Name: Government of Goa College of Engineering

Branch Name: Electronics (VLSI Design & Technology)

Examination : Semester VI May 2025

1 Seat No: 60798 PR Number: 202211433 Name: AKSHAY SANJEEV KULKARNI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	64 P	20		84 P			84	A	8	24
VLSI 620	Analog and Digital Communication	3	64 P	21		85 P			85	A	8	24
VLSI 633	Introduction to Micro-electromechanical Systems	3	66 P	14		80 P			80	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	73 P	20		93 P			93	A	8	24
VLSI 650	Embedded Design Lab	1			22 P	22 P	22P		44	O	10	10
VLSI 660	Programming Lab II	1			23 P	23 P	21P		44	O	10	10
VLSI 670	FPGA Design Lab	2			47 P	47 P	23P		70	O	10	20
CV633	Air and Noise Pollution and Control	3	53 P	19		72 P			72	B+	7	21
HM006	Cyber Law and IPR	3	69 P	20		89 P			89	A	8	24
@IOT-03	Ubiquitous Sensing, Computing And Communication	4	45 P	16		61 P	23P		84	B+	7	28

Total Weighted Grade Points:178

Entitlement Marks :--

Earned Credits:22

SGPA :8.09

2 Seat No: 60799 PR Number: 202211322 Name: ANKIT RAKESH MISHRA

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	61 P	10		71 P			71	B+	7	21
VLSI 620	Analog and Digital Communication	3	58 P	10		68 P			68	B	6	18
VLSI 633	Introduction to Micro-electromechanical Systems	3	66 P	16		82 P			82	A	8	24
VLSI 641	Elements of Radar System Engineering	3	79 P	19		98 P			98	A+	9	27
VLSI 650	Embedded Design Lab	1			18 P	18 P	20P		38	A+	9	9
VLSI 660	Programming Lab II	1			15 P	15 P	20P		35	A	8	8
VLSI 670	FPGA Design Lab	2			42 P	42 P	14P		56	A	8	16
CV633	Air and Noise Pollution and Control	3	55 P	13		68 P			68	B	6	18
HM006	Cyber Law and IPR	3	72 P	20		92 P			92	A	8	24

Total Weighted Grade Points:165

Entitlement Marks : #10

Earned Credits:22

SGPA :7.5

3 Seat No: 60800 PR Number: 202211429 Name: ARYA ANIL PARAB

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points

Result Not Changed

4 Seat No: 60803 PR Number: 202211694 Name: ASHWIN NARESH MALIK

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	44 P	10		54 P			54	P	4	12
VLSI 620	Analog and Digital Communication	3	40 P	10		50 P			50	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	43 P	9		52 P			52	P	4	12
VLSI 641	Elements of Radar System Engineering	3	58 P	17		75 P			75	B+	7	21
VLSI 650	Embedded Design Lab	1			10 P	10 P	19P		29	B+	7	7
VLSI 660	Programming Lab II	1			10 P	10 P	15P		25	B	6	6
VLSI 670	FPGA Design Lab	2			35 P	35 P	11P		46	B+	7	14
HM006	Cyber Law and IPR	3	61 P	11		72 P			72	B+	7	21
IT643	Computer Forensics and Cyber Security	3	71 P	12		83 P			83	A	8	24

Total Weighted Grade Points:129

Entitlement Marks :--

Earned Credits:22

SGPA :5.86

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College Name: Government of Goa College of Engineering

Branch Name: Electronics (VLSI Design & Technology)

Examination : Semester VI May 2025

5 Seat No: 60807 PR Number: 202211938 Name: GILBILE VEDANT LAXMAN

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	48 P	10		58 P			58	C	5	15
VLSI 620	Analog and Digital Communication	3	30 F	10		40 F			40	F	0	0
VLSI 633	Introduction to Micro-electromechanical Systems	3	43 P	10		53 P			53	P	4	12
VLSI 641	Elements of Radar System Engineering	3	60 P	14		74 P			74	B+	7	21
VLSI 650	Embedded Design Lab	1			14 P	14 P	17P		31	B+	7	7
VLSI 660	Programming Lab II	1			18 P	18 P	18P		36	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	14P		54	A	8	16
HM006	Cyber Law and IPR	3	65 P	16		81 P			81	B+	7	21
IT644	E Commerce	3	34 F	12		46 F			46	F	0	0

Total Weighted Grade Points:100

Entitlement Marks :--

Earned Credits:16

SGPA :--

6 Seat No: 60808 PR Number: 202211517 Name: GREESHMA HARISH NAYAK

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	70 P	19		89 P			89	A	8	24
VLSI 620	Analog and Digital Communication	3	52 P	18		70 P			70	B+	7	21
VLSI 633	Introduction to Micro-electromechanical Systems	3	61 P	24		85 P			85	A	8	24
VLSI 641	Elements of Radar System Engineering	3	74 P	21		95 P			95	A+	9	27
VLSI 650	Embedded Design Lab	1			21 P	21 P	21P		42	A+	9	9
VLSI 660	Programming Lab II	1			22 P	22 P	22P		44	O	10	10
VLSI 670	FPGA Design Lab	2			46 P	46 P	19P		65	O	10	20
HM006	Cyber Law and IPR	3	67 P	18		85 P			85	A	8	24
IT643	Computer Forensics and Cyber Security	3	69 P	16		85 P			85	A	8	24

Total Weighted Grade Points:183

Entitlement Marks :--

Earned Credits:22

SGPA :8.32

7 Seat No: 60810 PR Number: 202211494 Name: JOHAN JOAQUIM FERNANDES

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	62 P	14		76 P			76	B+	7	21
VLSI 620	Analog and Digital Communication	3	52 P	13		65 P			65	B	6	18
VLSI 633	Introduction to Micro-electromechanical Systems	3	55 P	15		70 P			70	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	64 P	16		80 P			80	B+	7	21
VLSI 650	Embedded Design Lab	1			18 P	18 P	20P		38	A+	9	9
VLSI 660	Programming Lab II	1			17 P	17 P	19P		36	A	8	8
VLSI 670	FPGA Design Lab	2			42 P	42 P	12P		54	A	8	16
HM006	Cyber Law and IPR	3	54 P	13		67 P			67	B	6	18
IT643	Computer Forensics and Cyber Security	3	64 P	13		77 P			77	B+	7	21

Total Weighted Grade Points:153

Entitlement Marks :--

Earned Credits:22

SGPA :6.95

8 Seat No: 60814 PR Number: 202211638 Name: MALVIKA GAJANAN KORGANONKER

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	75 P	19		94 P			94	A+	9	27
VLSI 620	Analog and Digital Communication	3	68 P	21		89 P			89	A	8	24
VLSI 633	Introduction to Micro-electromechanical Systems	3	73 P	21		94 P			94	A+	9	27
VLSI 641	Elements of Radar System Engineering	3	78 P	22		100 P			100	A+	9	27
VLSI 650	Embedded Design Lab	1			20 P	20 P	20P		40	A+	9	9
VLSI 660	Programming Lab II	1			23 P	23 P	18P		41	A+	9	9
VLSI 670	FPGA Design Lab	2			46 P	46 P	18P		64	O	10	20
CV633	Air and Noise Pollution and Control	3	53 P	19		72 P			72	B+	7	21
HM006	Cyber Law and IPR	3	73 P	19		92 P			92	A	8	24

Total Weighted Grade Points:188

Entitlement Marks :--

Earned Credits:22

SGPA :8.55

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9 Seat No: 60815 PR Number: 202311061 Name: MANDREKAR BALKRISHANA SHASHIKANT

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	41 P	14		55 P			55	P	4	12
VLSI 620	Analog and Digital Communication	3	52 P	10		62 P			62	C	5	15
VLSI 633	Introduction to Micro-electromechanical Systems	3	28 F	13		41 F			41	F	0	0
VLSI 641	Elements of Radar System Engineering	3	67 P	17		84 P			84	A	8	24
VLSI 650	Embedded Design Lab	1			19 P	19 P	15P		34	A	8	8
VLSI 660	Programming Lab II	1			21 P	21 P	21P		42	A+	9	9
VLSI 670	FPGA Design Lab	2			45 P	45 P	20P		65	O	10	20
EE644	Electrical Energy Conservation & Auditing	3	48 P	10		58 P			58	C	5	15
HM006	Cyber Law and IPR	3	48 P	16		64 P			64	B	6	18

Total Weighted Grade Points:121

Entitlement Marks :--

Earned Credits:19

SGPA :--

10 Seat No: 60817 PR Number: 202211519 Name: NARAYAN SHANKER SAWANT

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points

Result Not Changed

11 Seat No: 60818 PR Number: 202309925 Name: NIGEL MENDES

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	41 P	10		51 P			51	P	4	12
VLSI 620	Analog and Digital Communication	3	40 P	10		50 P			50	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	37 F	10		47 F			47	F	0	0
VLSI 641	Elements of Radar System Engineering	3	73 P	12		85 P			85	A	8	24
VLSI 650	Embedded Design Lab	1			16 P	16 P	16P		32	B+	7	7
VLSI 660	Programming Lab II	1			17 P	17 P	17P		34	A	8	8
VLSI 670	FPGA Design Lab	2			28 P	28 P	11P		39	B	6	12
HM006	Cyber Law and IPR	3	61 P	16		77 P			77	B+	7	21
ME644	Supply Chain Management	3	69 P	20		89 P			89	A	8	24

Total Weighted Grade Points:120

Entitlement Marks :--

Earned Credits:19

SGPA :--

12 Seat No: 60819 PR Number: 202211432 Name: PRASAD BALAJI ST TOKARI KOLI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	79 P	21		100 P			100	A+	9	27
VLSI 620	Analog and Digital Communication	3	54 P	18		72 P			72	B+	7	21
VLSI 633	Introduction to Micro-electromechanical Systems	3	65 P	18		83 P			83	A	8	24
VLSI 641	Elements of Radar System Engineering	3	85 P	22		107 P			107	O	10	30
VLSI 650	Embedded Design Lab	1			22 P	22 P	22P		44	O	10	10
VLSI 660	Programming Lab II	1			23 P	23 P	18P		41	A+	9	9
VLSI 670	FPGA Design Lab	2			47 P	47 P	22P		69	O	10	20
CV633	Air and Noise Pollution and Control	3	52 P	14		66 P			66	B	6	18
HM006	Cyber Law and IPR	3	69 P	20		89 P			89	A	8	24
@IOT-03	Ubiquitous Sensing, Computing And Communication	4	59 P	17		76 P	23P		99	A	8	32

Total Weighted Grade Points:183

Entitlement Marks :--

Earned Credits:22

SGPA :8.32

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13 Seat No: 60821 PR Number: 202211567 Name: PRERANA RAJENDRA GHADI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	87 P	24		111 P			111	O	10	30
VLSI 620	Analog and Digital Communication	3	69 P	17		86 P			86	A	8	24
VLSI 633	Introduction to Micro-electromechanical Systems	3	81 P	19		100 P			100	A+	9	27
VLSI 641	Elements of Radar System Engineering	3	90 P	25		115 P			115	O	10	30
VLSI 650	Embedded Design Lab	1			24 P	24 P	22P		46	O	10	10
VLSI 660	Programming Lab II	1			20 P	20 P	19P		39	A+	9	9
VLSI 670	FPGA Design Lab	2			47 P	47 P	20P		67	O	10	20
HM006	Cyber Law and IPR	3	57 P	22		79 P			79	B+	7	21
IT643	Computer Forensics and Cyber Security	3	87 P	14		101 P			101	A+	9	27

Total Weighted Grade Points:198

Entitlement Marks :--

Earned Credits:22

SGPA :9

14 Seat No: 60824 PR Number: 202211520 Name: RUDRESH RAJARAM RAUT

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	40 P	10		50 P			50	P	4	12
VLSI 620	Analog and Digital Communication	3	20 F	10		30 F			30	F	0	0
VLSI 633	Introduction to Micro-electromechanical Systems	3	26 F	11		37 F			37	F	0	0
VLSI 641	Elements of Radar System Engineering	3	62 P	15		77 P			77	B+	7	21
VLSI 650	Embedded Design Lab	1			18 P	18 P	17P		35	A	8	8
VLSI 660	Programming Lab II	1			14 P	14 P	19P		33	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	14P		54	A	8	16
CV633	Air and Noise Pollution and Control	3	43 P	10		53 P			53	P	4	12
HM006	Cyber Law and IPR	3	53 P	14		67 P			67	B	6	18

Total Weighted Grade Points:95

Entitlement Marks : #10

Earned Credits:16

SGPA :--

15 Seat No: 60825 PR Number: 202211695 Name: RUTAMBAR DILIP DESAI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	56 P	11		67 P			67	B	6	18
VLSI 620	Analog and Digital Communication	3	42 P	12		54 P			54	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	40 P	17		57 P			57	C	5	15
VLSI 641	Elements of Radar System Engineering	3	72 P	18		90 P			90	A	8	24
VLSI 650	Embedded Design Lab	1			20 P	20 P	18P		38	A+	9	9
VLSI 660	Programming Lab II	1			17 P	17 P	19P		36	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	13P		53	A	8	16
CV633	Air and Noise Pollution and Control	3	40 P	19		59 P			59	C	5	15
HM006	Cyber Law and IPR	3	66 P	16		82 P			82	A	8	24

Total Weighted Grade Points:141

Entitlement Marks : #10

Earned Credits:22

SGPA :6.41

16 Seat No: 60828 PR Number: 202211639 Name: SHAYYUB NABISAB SHAIKH SANDHI

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	34 F	10		44 F			44	F	0	0
VLSI 620	Analog and Digital Communication	3	46 P	10		56 P			56	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	28 F	13		41 F			41	F	0	0
VLSI 641	Elements of Radar System Engineering	3	75 P	15		90 P			90	A	8	24
VLSI 650	Embedded Design Lab	1			16 P	16 P	18P		34	A	8	8
VLSI 660	Programming Lab II	1			14 P	14 P	18P		32	B+	7	7
VLSI 670	FPGA Design Lab	2			38 P	38 P	11P		49	A	8	16
HM006	Cyber Law and IPR	3	60 P	18		78 P			78	B+	7	21
IT644	E Commerce	3	48 P	10		58 P			58	C	5	15

Total Weighted Grade Points:103

Entitlement Marks : #10

Earned Credits:16

SGPA :--

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College Name: Government of Goa College of Engineering

Branch Name: Electronics (VLSI Design & Technology)

Examination : Semester VI May 2025

17 Seat No: 60832 PR Number: 202211615 Name: SHREYA CHANDRAKANT KHOKARALKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	40 P	12		52 P			52	P	4	12
VLSI 620	Analog and Digital Communication	3	40 P	14		54 P			54	P	4	12
VLSI 633	Introduction to Micro-electromechanical Systems	3	43 P	12		55 P			55	P	4	12
VLSI 641	Elements of Radar System Engineering	3	77 P	22		99 P			99	A+	9	27
VLSI 650	Embedded Design Lab	1			21 P	21 P	20P		41	A+	9	9
VLSI 660	Programming Lab II	1			18 P	18 P	19P		37	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	12P		52	A	8	16
HM006	Cyber Law and IPR	3	57 P	11		68 P			68	B	6	18
IT643	Computer Forensics and Cyber Security	3	69 P	16		85 P			85	A	8	24

Total Weighted Grade Points:138

Entitlement Marks :--

Earned Credits:22

SGPA :6.27

18 Seat No: 60833 PR Number: 202211282 Name: SHREYASH PRAVIN P USGAONKAR

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	77 P	10		87 P			87	A	8	24
VLSI 620	Analog and Digital Communication	3	54 P	11		65 P			65	B	6	18
VLSI 633	Introduction to Micro-electromechanical Systems	3	63 P	11		74 P			74	B+	7	21
VLSI 641	Elements of Radar System Engineering	3	90 P	12		102 P			102	A+	9	27
VLSI 650	Embedded Design Lab	1			15 P	15 P	19P		34	A	8	8
VLSI 660	Programming Lab II	1			16 P	16 P	17P		33	A	8	8
VLSI 670	FPGA Design Lab	2			40 P	40 P	18P		58	A+	9	18
CV633	Air and Noise Pollution and Control	3	69 P	6		75 P			75	B+	7	21
HM006	Cyber Law and IPR	3	67 P	10		77 P			77	B+	7	21

Total Weighted Grade Points:166

Entitlement Marks : #12

Earned Credits:22

SGPA :7.55

19 Seat No: 60837 PR Number: 202311067 Name: SUYASH NAIK

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points
VLSI 610	Embedded Systems with RTOS	3	40 P	10		50 P			50	P	4	12
VLSI 620	Analog and Digital Communication	3	Ab	10		10 F			10	Ab	0	0
VLSI 633	Introduction to Micro-electromechanical Systems	3	50 P	11		61 P			61	C	5	15
VLSI 641	Elements of Radar System Engineering	3	63 P	16		79 P			79	B+	7	21
VLSI 650	Embedded Design Lab	1			16 P	16 P	16P		32	B+	7	7
VLSI 660	Programming Lab II	1			15 P	15 P	17P		32	B+	7	7
VLSI 670	FPGA Design Lab	2			25 P	25 P	5F		30	F	0	0
EE644	Electrical Energy Conservation & Auditing	3	41 P	10		51 P			51	P	4	12
HM006	Cyber Law and IPR	3	57 P	14		71 P			71	B+	7	21

Total Weighted Grade Points:95

Entitlement Marks :--

Earned Credits:17

SGPA :--

20 Seat No: 60840 PR Number: 202211412 Name: VAISHNAVI KRISHNANAND NAIK

Paper Code	Paper Name	Paper Credits	Theory	IA	Term Work	Theory Total	Practical	Oral	Total	Grade	Grade Point	Weighted Grade Points

Result Not Changed

Read By:

Checked By:

Date : 27 OCT 2025

Asstt. Registrar Exam(Prof)
(Technical and Allied)

Asstt. Registrar Exam

Controller of Examinations